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E-mail: rashmi.priyadarshini@sharda.ac.in
Mobile: 9911450973

Submitted for consideration and the approval of Dean, SET/ Vice-Chancellor, SU

AK Singh
Dr. A. K. Singh

~~Dean SET~~

May like to approve

AK
10/11/16

~~Vice-Chancellor~~

*Please add me
more person in
Category B.*

Dr. A. K. Singh

Minutes of meeting of BOS of ECE held on June 11, 2015

A meeting of the Board of studies was held on 11th June 2015 at Block-I 402, ECE Boardroom at 2:00 pm.

The following members were present.

1. Prof. Raj Senani, NSIT
2. Prof. Avinashi Kapoor, University of Delhi
3. Dr. Amitava Majumdar, Moser Baer India
4. Prof. R. M. Mehra
5. Prof. Abdhesh Singh (HoD), ECE
6. Dr. Pallavi Gupta
7. Mr. Sandeep Singh
8. Ms Shaheen Naz

The course structure & syllabi of the following courses were considered.

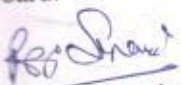
B.Tech. In Electronics and Communication

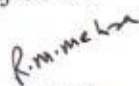
M.Tech.in ECE with Specialization:

1. Microwave Technology
2. VLSI Technology
3. Digital Communication (NEW)
4. Electronic System Design and Management (NEW)
5. Optical Communication (NEW)
6. Digital Signal Processing (NEW)


All the course structures and syllabi were discussed and approved with minor modification in the syllabus of the subject EMFT. One unit of transmission lines be included. The change has been incorporated.

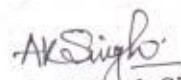
It is recommended that the details of the above approved courses be placed before the faculty Board.

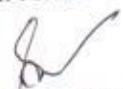

Prof. Raj Senani



Prof. R. M. Mehra

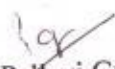

Mr. Sandeep Singh


Prof. Avinashi Kapoor


Prof. Abdhesh Singh


Ms Shaheen Naz


Dr. Amitava Majumdar


Dr. Pallavi Gupta

B.Tech.

in

Electronics and Communication Engineering
COURSE STRUCTURE & SYLLABI

(2nd to 4th Year With effect from academic session 2015-16)



Department of Electronics and Communication Engineering
School of Engineering and Technology
SHARDA UNIVERSITY

B.Tech First Year Course Structure for 2015

Sem.	COURSE STRUCTURE						Courses	L	T	P	Weekly Contact	Credit
	Physics-2 PHY-112 (3-1-2)5	Logic Building in problem solving CSE-105 (3-1-2)5	Mathematics-1 MTH-112 (3-2-0)4	Env.Sc. EVS-103 (2-0-0)2	Manufacturing Science EEE 154 (0-0-4)2	Functional English-I ENG-102 (2-0-2)3						
I							6	4	10		27	21
II	Chemistry CHY-105 (3-1-2)5	Programming using 'C' CSE-106 (3-0-2)4	Mathematics-2 MTH-113 (3-2-0)4	Physics-4 PHY-114 (3-1-0)4	PEEE EEE-110 (3-1-2)5	Functional English-II ENG-103 (2-0-2)3	6	5	8		30	25
											Total	46

B. Tech ECE- Session 2012-2016

Sem.	Courses (L-T-P)										Courses	Contact Hours				Credits
	III	IV	V	VI	Summer Internship (ECE334)					VII		VII I	L	T	P	
III	Analog Circuits (ECE231)(DC) (3-1-2) 5	Digital Electronics- (ECE232)(DC) (3-1-2) 5	Mathematics-3 (Engg Maths) (MTH217) (3-1-0) 4	Managerial Economics (HMM206) (3-0-0) 3	Signals and Systems (ECE233) (DC) (3-1-0) 4	Term Paper (ECE281) (0-0-2) 1	6	15	4	6	24	22				
IV	Digital Electronics- II (DC) (ECE234) (3-1-0) 4	Network Analysis and Synthesis (DC) (ECE237) (3-1-2) 5	EMFT (DC) (ECE235) (3-1-0) 4	Communication Practices (ENG201) (2-0-2) 3	Microprocessors and interfacing (ECE236)(DC) (3-1-2) 5	Term Paper (ECE282) (0-0-2) 1	7	17	4	10	30	26				
V	Linear Integrated Circuit (ECE331) (DC) (3-1-2) 5	Communication Engg. (ECE332) (DC) (3-1-0) 4	DE I (3-1-2) 5	OE I (4-0-0) 4	DSP (DC) (ECE333) (3-1-2) 5	Term Paper (ECE283) (0-0-2) 1	7	19	4	8	30	27				
VI	Digital Communication (DC) (ECE334) (3-1-2) 5	DEII (3-1-2) 5	Microcontroller s and Applications (DC) (ECE335) (3-1-2) 5	OE II (4-0-0) 4	Professional Skills Enhancement t(ENG301) (2-0-2) 3	Term Paper (ECE284) (0-0-2) 1	6	15	3	10	27	23				
VII	CMOS VLSI Design (DC) (ECE431) (3-1-2) 5	DE III (3-0-0) 3	DE IV (3-0-2) 4	OE III (4-0-0) 4	Human Resource Management (HMM405) (3-0-0) 3	Project 2 (ECE491)	0	0	0	0	0	02				
VII I	DE V (3-0-0)3	OE IV (4-0-0) 4	DE VI (3-0-0) 3	Project 8 (ECE492)	Writing For Technical For Purposes (ENG401)(3-0-0)3		5	13	0	0	13	21				
TOTAL											12+ 37= 49	27+95 =122	3+ 16= 19	18+3 8=56 96	51+ 145= 196	41+ 142= 183

B. Tech Course Structure for 2015-16

Sem.	Courses (L-T-P)							Courses	Contact Hours				Credits	
	Analog Circuits (DC) (3-1-2) 5 NEW <i>ECE-221</i>	Digital Electronics (DC) (3-1-2) 5 <i>ECC-222</i>	Networks and Systems (3-1-2) 5 <i>ECE-223</i>	Mathematics-3 Engg Maths MTH217 /Introductory Mathematics* MTH216 (3-1-0) 4	Communication Practices ENG202 (2-0-2) 3	Term Paper ECP280 (0-0-2) 1	5		L	T	P	Total		
III														
IV	Analog and Digital Circuits (DC) (3-1-2) 5 NEW	Microprocessor and Interfacing ECE236 (DC) (3-0-2) 4	EMFT (DC) ECE235 (3-1-0) 4	Concepts of OOPS programming using Java CMP202 (3-1-2) 5	Economics for Engineers HMM206 (3-0-0) 3	Term Paper ECP281 (0-0-2) 1	5	15	3	8	26		22	
V	Linear Integrated Circuit (DC) ECE331 (3-1-2) 5	Communication Engg. (DC) ECE332 (3-1-2) 5	Digital Signal Processing (DC) ECE333 (3-1-2) 5	Microwave & Radar Engineering (DC) (3-0-2) 5 NEW	Professional Skills Enhancement (2-0-2) 4 ENG301	Term Paper ECP380 (0-0-2) 1	5	15	3	12	30		24	
VI	Digital Communication (DC) ECE334 (3-1-2) 5	Microcontrollers and Applications (DC) ECE 335 (3-0-2) 4	Control System (3-1-2) 5 (DC) ECE341 NEW	Management for Engineers HMM303 (3-0-0) 3	DEI/Math 3* (3-1-0) 4	Term Paper ECP381 (0-0-2) 1	5	15	3	8	26		22	
Summer Internship ECE394 (0-0-4) 2													2	
VII	CMOS VLSI Design (DC) ECE431 (3-1-2) 5	Advanced Written Communication ENGXXX (3-0-0) 3	Project (0-0-4) 2 ECE490	OE I (3-0-0) 3	DE II (3-1-0) 4	DE III (3-1-2) 5	6	15	3	8	26		22	
VIII	DE IV (3-1-0) 4	OE II (3-0-0) 3	DE V (3-1-0) 4	Project ECE491 (0-0-16) 8	Project Management HMNXXX (3-0-0) 3		5	12	2	16	30		22	
TOTAL							12+ 41= 51	27+ 86= 113	3+1 8= 21	18+ 62= 80	51+ 154= 205		46+ 137= 183	

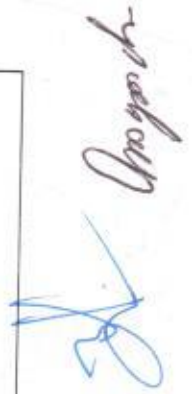
*For Diploma Holders only

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Bas recommends - [Signature]
Approved [Signature]

Some Modifications/Changes are as Follows for B. Tech 2015

Present B. Tech Courses	Modifications/Changes for B. Tech Courses 2015
Signals and Systems	Networks and Systems ECE 237 III Semester
Networks analysis and Synthesis	
Analog Circuits-I	Analog Circuits Syllabus changed in III semester
Digital electronic II	Merged in Analog and Digital Circuits (New Subject) IV Semester
Managerial Economics	Renamed Economics For Engineers, shifted from III to IV Semester
Communications Practices	Shifted from IV to III Semester
Maths-3	Introductory Mathematics for Diploma Holders and maths-3 for all others in III Semester
Concepts of OOPS programming using C++ CMP202 in IV Semester	Renamed Concepts of OOPS programming using JAVA CMP202 in IV Semester
Microwave & radar Engineering is in departmental elective	Microwave & Radar Engineering included in 5 th semester
Professional skills enhancement	Professional skills enhancement shifted to V semester to VI semester
Organizational behavior	Organizational behavior renamed as Management for engineers now in VI semester shifted from V semester
Control system is in DE	Control system included in as core course in VI semester
Mathematics- 3	Mathematics- 3 is a Departmental Elective for diploma holders (DE-1)
Human resource management	Renamed as project management shifted to VIII semester from VII semester
Writing for technical purposes	Renamed as Advanced Written semester and shifted to VII semester from VIII semester
Department electives are six	Reduced to five
Open elective are four	Reduced to two of each three credits

P.S. Sharma
11/6/15
Bos recommends



Departmental electives

S.No	School	Discipline	Degree	Term	Course title	L	T	P	Credits	Compulsory or Elective
1	SET	ECE	B.Tech.	6	Mixed Signal CMOS VLSI Design	3	1	0	4	Departmental Elective-1
2	SET	ECE	B.Tech.	6	Real Time operating Systems	3	1	0	4	
3	SET	ECE	B.Tech.	6	Antennas & Wave Propagation	3	1	0	4	
4	SET	ECE	B.Tech.	7	VHDL Design	3	1	0	4	
5	SET	ECE	B.Tech.	7	Programming and Customizing the AVR Microcontroller	3	1	0	4	Departmental Elective-2
6	SET	ECE	B.Tech.	7	RF & Microstrip Antennas	3	1	0	4	Departmental Elective-3
7	SET	ECE	B.Tech.	7	Optical Communications	3	1	2	5	
8	SET	ECE	B.Tech.	7	Image Processing	3	1	2	5	
9	SET	ECE	B.Tech.	7	ASIC/FPGA Design	3	1	2	5	
10	SET	ECE	B.Tech.	7	Advanced Communication System	3	1	2	5	
11	SET	ECE	B.Tech.	8	Telecommunication Switching	3	1	0	4	Departmental Elective-4
12	SET	ECE	B.Tech.	8	Satellite Communications	3	1	0	4	
13	SET	ECE	B.Tech.	8	Data Communication Network	3	1	0	4	
14	SET	ECE	B.Tech.	8	Wireless & Mobile Communication	3	1	0	4	
15	SET	ECE	B.Tech.	8	CMOS: Circuit Design, Layout and Simulation	3	1	0	4	Departmental Elective-5
16	SET	ECE	B.Tech.	8	Embedded System	3	1	0	4	
17	SET	ECE	B.Tech.	8	CAD for Patch Antennas Design	3	1	0	4	

Following new subjects has been added in departmental electives

S.No	School	Discipline	Degree	Term	Course code	Course title	L	T	P	Credits	Compulsory or Elective
1	SET	ECE	B.Tech.	5	NEW	CMOS: Circuit Design, Layout and Simulation	3	0	0	3	Departmental Elective-1
2	SET	ECE	B.Tech.	6	NEW	RF and Microstrip patch Antenna	3	0	0	3	Departmental Elective-2
3	SET	ECE	B.Tech.	7	NEW	Real Time operating Systems	3	0	0	3	Departmental Elective-3
4	SET	ECE	B.Tech.	7	NEW	CAD of Patch Antenna for Wireless applications	3	0	2	4	Departmental Elective-4
5	SET	ECE	B.Tech.	8	NEW	Programming and Customizing the AVR Microcontroller	3	0	0	3	Departmental Elective-5
6	SET	ECE	B.Tech.	8	NEW	Mixed Signal CMOS VLSI Design	3	0	0	3	
7	SET	ECE	B.Tech.	8	NEW	Embedded RF System and high frequency Signal Processing	3	0	0	3	

Approved
BOS recommends
11/11/15

Open electives

S.No	School	Discipline	Degree	Term	Course code	Course title	L	T	P	Credits	Compulsory or Elective
1	SET	ECE	B.Tech.	7	ECE438	Photovoltaic Devices	3	0	0	3	Open Elective-1
2	SET	ECE	B.Tech.	7	ECE471	Consumer Electronics	3	0	0	3	
3	SET	ECE	B.Tech.	7	ECE337	Digital Logic Design	3	0	0	3	
6	SET	ECE	B.Tech.	8	ECE467	Bio-medical Instrumentation	3	0	0	3	Open Elective-2
8	SET	ECE	B.Tech.	8	ECE349	Transducers & Sensor	3	0	0	3	
9	SET	ECE	B.Tech.	8	ECE462	Smart Sensors And Sensors Networking	3	0	0	3	
10	SET	ECE	B.Tech.	8	ECE239	Electrical And Electronic Measurement And Instrumentation	3	0	0	3	

Following new subjects has been added in Open electives

S.No	School	Discipline	Degree	Term	Course code	Course title	L	T	P	Credits	Compulsory or Elective
1	SET	ECE	B.Tech.	7	NEW	Bio-Instrumentation	3	0	0	3	Open Elective-1
2	SET	ECE	B.Tech.	7	NEW	User Interface Design	3	0	0	3	
3	SET	ECE	B.Tech.	8	NEW	Mobile Computing	3	0	0	3	Open Elective-2
4	SET	ECE	B.Tech.	8	NEW	Nano-Electronics	3	0	0	3	

Approved

Recommendations

BoS

BoS

M.Tech.

in

*Electronics and Communication Engineering
(With Specialization in Digital Communication)*

COURSE STRUCTURE & SYLLABI

(With effect from academic session 2015-17)



*Department of Electronics and Communication
Engineering
School of Engineering and Technology
SHARDA UNIVERSITY*

Department Electives

School	Discipline	Degree	Term	Course code	Course title	L	T	P	Credits	Compulsory or Elective
SET	ECE	M.Tech.		ECE 665	Neural Networks	3	0	0	3	Departmental Elective-1
SET	ECE	M.Tech.		ECE 686	Microwave Communication	3	0	0	3	Departmental Elective-1
SET	ECE	M.Tech.		ECE 687	Design of Communication Networks	3	0	0	3	Departmental Elective-2
SET	ECE	M.Tech.		ECE 667	Data Communication	3	0	0	3	Departmental Elective-2
SET	ECE	M.Tech.		ECE 669	Secure Communication	3	0	0	3	Departmental Elective-3
SET	ECE	M.Tech.		ECE 688	Pattern Recognition and Analysis	3	0	0	3	Departmental Elective-3
SET	ECE	M.Tech.		ECE 689	Cryptography	3	0	0	3	Departmental Elective-3

Open Electives Offered by Department

School	Discipline	Degree	Term	Course code	Course title	L	T	P	Credits	Compulsory or Elective
SET	ECE	M.Tech.		ECE 777	Cellular and Mobile Communication	3	0	0	3	Open Elective-1
SET	ECE	M.Tech.		ECE 619	Nanoelectronics	3	0	0	3	Open Elective-1
SET	ECE	M.Tech.		ECE 640	RF and Micro-MEMS	3	0	0	3	Open Elective-1

Course Structure

M.Tech. in ECE with specialization in Digital Communication

(Degree by Course work and Dissertation)

		Courses (L-T-P)						Lecture Courses				Contact Hr/Week				Credits
								L	T	P	Total	L	T	P	Total	
Computational Methods for Comm. ECE683 (DC) (3-1-0) 4	Discrete time signal processing ECE684 (3-0-0) 3	Advanced Digital Comm. ECE685(3-0-0) 3	DE 1 (3-0-0) 3	DE-2 (3-0-0) 3	ENP601 Technical Presentation (1-0-2) 2 (004)2	Digital Signal Processing Lab ECP684 (0-0-4)2	6	16	1	6	23	20				
Information Theory & Coding ECE771(DC) (3-0-0)3	Detection & Estimation Theory ECE772 (3-0-0) 3	Modern Telecom Switching Systems ECE773 (3-0-0) 3	OE 1 (3-0-0) 3	DE 3 (3-0-0) 3	Term Paper ECP681 (0-2-0) 2	Communication Lab ECP685 (0-0-4)2	6	16	0	6	22	19				
Seminar ECE781 (0-0-4)2	Project ECE782 (0-0-8)4	Dissertation-I ECE783 (0-0-20)10					0	0	0	28	28	16				
Dissertation-II ECE784 (0-0-32)16							0	0	0	32	32	16				
C-X-Y (Course-Semester-Paper No.); L-lecture, T-tutorial, P-practical												Total Credits -71				

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Course Structure

M.Tech. in ECE with specialization in Digital Communication

SEMESTER	Courses (L-T-P)						Contact Hr/Week					
							Lecture Courses				Total	
	L	T	P									
I	C-1-1 Computational Methods for Comm (DC) (3-1-0) 4	C-1- Discrete time signal processing (3-0-0) 3	C-1-3 Advanced Digital Comm. (3-0-0) 3	C-1-4 DE 1 (3-0-0) 3	C-1-5 OE-1 (3-0-0) 3	Technical Presentation (1-0-2) 2	Digital Signal Processing Lab (0-0-4)2	6	16	1	6	23
II	C-2-1 Information Theory & Coding (DC) (3-0-0)3	C-2 Detection & Estimation Theory (3-0-0) 3	C-2-3 Modern Telecom Switching Systems (3-0-0) 3 DE 3	C-2-4 DE 2 (3-0-0) 3	C-2-5 DE 3 (3-0-0) 3	Term Paper (0-2-0) 2	Communication Lab Lab (0-0-4)2	6	16	0	6	22
III	Seminar (0-0-4)2	Project (0-0-8)4	Dissertation-I (0-0-20)10					0	0	0	28	28
IV	Dissertation-II (0-0-32)16							0	0	0	32	32

C-X-Y (Course-Semester-Paper No.): L-lecture, T-tutorial, P-practical

Handwritten notes:
 16/11/15
 BOS recommends

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Total Credits -

LIST OF DEPARTMENTAL ELECTIVES

- Neural Networks
- Microwave Communication
- Secure Communication
- Design of Communication Networks
- Pattern Recognition and Analysis
- Data Communication
- Cryptography

LIST OF OPEN ELECTIVES

- Wireless and Mobile Communication
- Nano-electronics
- RF and Micro-MEMS

Appendix
Recommended
Books
Feb 15/15

Course Structure

M.Tech. in ECE with Specialization in Electronic System Design and Management

SEMESTER	Courses (L-T-P)						Contact Hr/Week				Credits	
	C-1-1	C-1-2	C-1-3	C-1-4	C-1-5	Technical Presentation (1-0-2) 2	Embedded Systems and Design Lab(0-0-4)2	L	T	P		Total
I	C-1-1 Portable electronics product design and development (DC) (3-1-0) 4	C-1-2 Device modeling and circuit simulation (DC) (3-0-0) 3	C-1-3 Embedded system design (DC) (3-0-0) 3	C-1-4 DE 1 (3-0-0) 3	C-1-5 OE-1 (3-0-0) 3	Technical Presentation (1-0-2) 2	Embedded Systems and Design Lab(0-0-4)2	16	1	6	23	20
II	C-2-1 EMI & EMC of electrical equipments (DC) (3-0-0)3	C-2-2 Thermal energy management and conservation (DC) (3-0-0) 3	C-2-3 VLSI Design (DC) (3-0-0) 3	C-2-4 DE 2 (3-0-0) 3	C-2-5 DE 3 (3-0-0) 3	Term Paper (0-2-0) 2	E-CAD Lab (0-0-4)2	16	0	6	22	19
III	Seminar (0-0-4)2	Project (0-0-8)4	Dissertation-I (0-0-20)10					0	0	28	28	16
IV	Dissertation-II (0-0-32)16							0	0	32	32	16

C-X-Y (Course-Semester-Paper No.); L-lecture, T-tutorial, P-practical

Total Credits -71

Dr. P. V. S. Rao
Dr. K. S. Ramesh
Dr. K. S. Ramesh
Dr. K. S. Ramesh

LIST OF DEPARTMENTAL ELECTIVES/OPEN ELECTIVES

- Financial Accounting
- Energy modeling ,economics and project managment
- Electronics and electrical measurement and instrumentation
- Contracts Laws & regulations
- compact and broadband microstrip antennas design
- consumer electronics

LIST OF OPEN ELECTIVES

- Yield and Reliability Engineering
- Digital signal processing
- Advanced material for thermal management of electronics packaging

Approved
11.6.15
Bos Recommendations
Meyer

11/15

Minutes of meeting of BOS of ECE held on June 11, 2015

A meeting of the Board of studies was held on 11th June 2015 at Block-I 402, ECE Boardroom at 2:00 pm.

The following members were present.

1. Prof. Raj Senani, NSIT
2. Prof. Avinashi Kapoor, University of Delhi
3. Dr. Amitava Majumdar, Moser Baer India
4. Prof. R. M. Mehra
5. Prof. Abdhesh Singh (HoD), ECE
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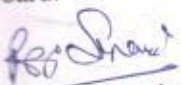
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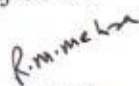
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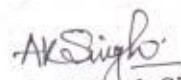
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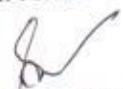

Prof. Raj Senani



Prof. R. M. Mehra

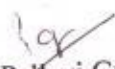

Mr. Sandeep Singh


Prof. Avinashi Kapoor


Prof. Abdhesh Singh


Ms Shaheen Naz


Dr. Amitava Majumdar


Dr. Pallavi Gupta

Approval for course structure

2 messages

Dr. Abdhesh Kumar Singh <abdhesh.singh@sharda.ac.in>

Wed, May 31, 2017 at 2:13 PM

To: Raj Senani <dr_senani@yahoo.com>, avinashi_kapoor@yahoo.com

Dear Sir

Pl find attached herewith the course structure of 2016-20 and 2017-21 along with modifications/changes. The documents are as follows:

- Course Structure 2016-2020 (No change in 1st Year)
- Course Structure 2017-2021.
- Modification/Changes.

This needs your kind approval as a BOS member.

—

Thanks & Regards

Dr. A.K. Singh


Head of Department


Electronics & Communication Department


(91+9891410081)

Go Green, save Water,

3 attachments

 ECE.course Structure.2016-20.doc
85K

 ECE.course Structure.2017-21.doc
90K

 Modification in course structure.docx
18K

Dr. Raj Senani <dr_senani@yahoo.com>

Wed, May 31, 2017 at 2:47 PM

Reply-To: "Dr. Raj Senani" <dr_senani@yahoo.com>

To: "Dr. Abdhesh Kumar Singh" <abdhesh.singh@sharda.ac.in>

Dear Prof Singh

The attached course structure is hereby Approved.

sincerely,

Dr. Raj Senani

Member Sigma Xi, Senior Member IEEE, FIE, FIETE, F.N.A.Sc.
(former Director, NSIT: 2008-2014)
Professor,
Division of Electronics and Communication Engineering
Netaji Subhas Institute of Technology
Sector 3, Dwarka
New Delhi 110078

e-mail: senani@ieee.org
Mobile: +91-9899168678
Official web-page: <http://www.nsit.ac.in/faculty/rs/>
Personal home page: www.angelfire.com/in/RajSenani97

* Also working as Adjunct Professor, National Institute of Technology, Patna, India.

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Course Structure

M.Tech. in ECE with Specialization in Electronic System Designing and Management

SEMESTER	Courses (L-T-P)										Lecture Courses			Contact Hr/Week			Credits
	C-1-1 Emerging Electronics Technologies (DC) (3-1-0) 4	C-1-2 Methods in Product Development for Electronic Subsystems (DC) (3-0-0) 3	C-1-3 International Project Management (DC) (3-0-0) 3	C-1-4 DE 1 (3-0-0) 3	C-1-5 DE 2 (3-0-0) 3	Foreign Language (1-0-2) 2	E-CAD Lab (0-0-4) 2	L	T	P	Total	L	T	P	Total		
I	C-2-1 Communication Technologies & System (DC) (3-0-0) 3	C-2-2 Quality Management of Electronic Systems (DC) (3-0-0) 3	C-2-3 Embedded Systems (DC) (3-0-0) 3	C-2-4 DE 3 (3-0-0) 3	C-2-5 OE-1 (3-0-0) 3	Technical Presentation (1-0-2) 2	Embedded Systems and Design Lab (0-0-4) 2	16	1	6	23	6	1	6	20		
II	Seminar (0-0-4) 2	Project (0-0-8) 4	Dissertation-I (0-0-20) 10					0	0	28	28	0	0	28	16		
III	Dissertation-II (0-0-32) 16							0	0	32	32	0	0	32	16		
C-X-Y (Course-Semester-Paper No.); L-lecture, T-tutorial, P-practical															Total Credits -71		

VI D 588

Dissertation



SHARDA UNIVERSITY
SCHOOL OF ENGINEERING & TECHNOLOGY
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Date: 04/06/2018

A meeting of board of studies of ECE will be held on 5th June 2018, at 01:00 PM in Room No.005, block I

Chairman: Dr. Pallavi Gupta, HOD

LIST OF PROPOSED MEMBERS OF BOARD OF STUDIES (BOS)

- A. From University**
1. **Prof. Avinashi Kapoor**
Department of Electronic Science
University of Delhi South Campus
New Delhi-110 021
E-mail: avinashi_kapoor@yahoo.com
Mobile: 09350571397
 2. **Dr Ajay Agarwal**
CEERI, CSIR,
Pilani
Mob - 8058598903
- B. From Industry**
1. **Mr. Shekhar Singhal**
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Email ID- shekhar.singhal6@gmail.com
Mobile- 08527801799
 2. **Dr. Rajeev Kumar Srivastava**
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Mobile: 07259219237
 3. **Mr. Alok Mittal**
Senior Technical Officer
Advanced manufacturing lab
ST Microelectronics, Greater Noida,
E.Mail: alok.mittal@st.com,
Mobile: 9810060432
- C. Internal BOS members**
1. **Prof. R. M. Mehra**
E-mail: rm.mehra@sharda.ac.in



SHARDA UNIVERSITY
SCHOOL OF ENGINEERING & TECHNOLOGY
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

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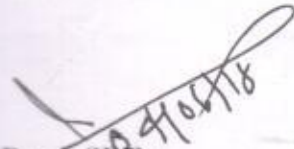
- A. From University**
1. **Prof. Avinashi Kapoor**
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E-mail: avinashi_kapoor@yahoo.com
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 2. **Dr Ajay Agarwal**
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- B. From Industry**
1. **Mr. Shekhar Singhal**
Module Lead,
Logic Fruit Technologies,
Sector 30, Gurugram, India
Email ID- shekhar.singhal6@gmail.com
Mobile- 08527801799
 2. **Dr. Rajeev Kumar Srivastava**
Director
Design Platforms, NXP Semiconductors India Private Limited
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E-mail: rajeev.rajeevsrivastava@gmail.com
Mobile: 07259219237
 3. **Mr. Alok Mittal**
Senior Technical Officer
Advanced manufacturing lab
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E.Mail: alok.mittal@st.com,
Mobile: 9810060432
- C. Internal BOS members**
1. **Prof. R. M. Mehra**
E-mail: rm.mehra@sharda.ac.in

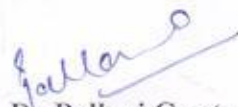
- Mobile: 9971271374
2. **Dr. Rashmi Priyadarshini**
E-mail: rashmi.priyadarshini@sharda.ac.in
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 3. **Mr. Sandeep Singh**
E-mail: sandeepsingh.ec@sharda.ac.in
Mobile: 9891833637
 4. **Mrs. Shaheen Naaz**
Email: Shaheen.naaz@sharda.ac.in
Mobile: 989899454758
 5. **Mr. Shiv Pujan Jaiswal**
EMail: shivapujan.jaiswal@sharda.ac.in
Mobile: 8826998122
 6. **Mr. C. Mohan**
EMail: c.mohan@sharda.ac.in
Mobile: 9811090352

Following are the agenda of the meeting :

1. Approval of new Course Structure of B.Tech., Electronics & Communication Engineering for 2018 Batch. (160 Credit)
2. Approval of new program of B.Sc (Hons) Electronics with specialization in Robotics and Application.
3. Approval of Syllabi of Above programs.
4. Approval of few syllabi of B.Tech. Electrical & Electronics Engineering, B.Tech. CSE with specialization in IoT.
5. Approval of few syllabi of M. Tech. Programmes.
6. Any other item.

All members are requested to make it convenient to attend the meeting.


Dean, SET


Dr. Pallavi Gupta

HOD, ECE & EE Deptt.

Department of Electronic and Communication Engineering
School of Engineering and Technology
Sharda University
Minutes of Meeting of External BoS(session 2018 onwards)

Following members were present in the External BoS for various programs on 05/06/2018 at 005, board room, block-I Chaired by Dr. Pallavi Gupta, HoD, ECE&EEE

A. From other University

1. Prof. Avinashi Kapoor
Department of Electronic Science
University of Delhi South Campus
New Delhi-110 021
E-mail: avinashi_kapoor@yahoo.com
Mobile: 09350571397

2: Dr Ajay Agarwal

CEERI, CSIR,
Pilani
Mob o8058598903

B. From Industry

1. **Mr. Shekhar Singhal**
Module Lead,
Logic Fruit Technologies,
Sector 30, Gurugram, India
Email ID- shekhar.singhal6@gmail.com
Mobile- 08527801799

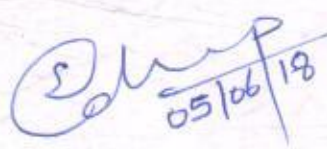
2. Dr. Rajeev Kumar Srivastava
Director
Design Platforms, NXP Semiconductors India Private Limited
Information Technology Park
Nagawara Village, Kasaba Hobli
Bangalore 560045
E-mail: rajeev.rajeevsrivastava@gmail.com
Mobile: 07259219237

3. Mr.Alok Mittal
Senior Technical Officer
Advanced manufacturing lab

ST Microelectronics
Greater Noida
E.Mail:Alok.mittal@st.com
Mobile:9810060432

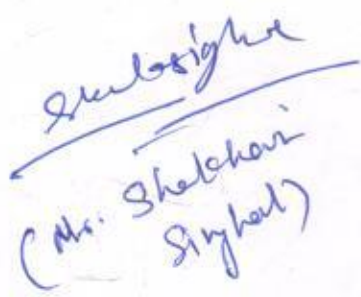
C. Internal members

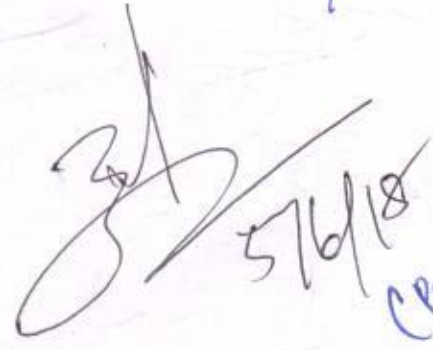
1. Dr. Pallavi Gupta(HOD)
(Associate Professor)
2. Prof. R. M. Mehra
3. Dr. Rashmi Priyadarshini
4. Mr. Sandeep Singh
5. Mrs. Shaheen Naaz
6. Mr. Shiv Pujan Jaiswal
7. Mr. C Mohan
8. Ms Manisha Rajoriya
9. Ms. Ritu Singh
10. Mr. Prince Nagar


05/06/18

Manisha Rajoriya




(Mr. Shalochan Singh)


5/6/18
(Prof. Anilashi Kapur)







Minutes B.Tech Course Structure - ECE

1. In Introduction to Engineers syllabus to be modified including Blue Brain, 5G, in place of AI.
2. Include subject as Smart Materials as elective.
3. Summer Internship in 1st year is to be reviewed.
4. Soft skill may be changed to audit course, to save our credits for core subjects like LIC.
5. Introduction to biology may be changed to Biomedical Engineering.
6. In DSP Book, include book DSP BY Rabiner and Gold, Pearson publication.
7. In tinkering lab syllabus, update syllabus of unit 3 and 4.
8. In Lab we should include protocol analyzer (open source).
9. PCB Design tools may be incorporated in syllabus of Tinkering lab.
10. Security and power saving techniques may be included in syllabus of IOT and Basics of drone technology
11. In 6th semester, if students are taking Embedded system as an elective, the project based learning will be lab for Embedded system based on STM32.
12. There should be one additional subject may be included in the program including differential amplifier, level shifter and current mirror and CMOS Technology.

Sen
anil

Mulligal

Edup
05/06/18

Pallavi

for

[Signature]

Some changes are as follows

1. Open elective shifted to Ist semester
2. Technical presentation shifted to Ist semester
3. Language shifted to IInd semester renamed as a Term Paper of (credit0-2-0)

New proposed M.Tech programmes are as follows :

1. M.Tech in ECE specialization in Digital communication.
2. M.Tech in ECE specialization in Digital Signal Processing.
3. M.Tech in ECE specialization in Optical Communication.
4. M.Tech in ECE specialization in Electronics system in Designing & Management.

Atchya



bos recommends

RJ
11/6/15

Amrinder

m

Some changes are as follows

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Atchya



bos recommends

RJ
11/6/15

Amrinder

m

Course Structure

M.Tech. in ECE with Specialization in Electronic System Design and Management

SEMESTER	Courses (L-T-P)						Contact Hr/Week				Credits	
	C-1-1	C-1-2	C-1-3	C-1-4	C-1-5	Technical Presentation (1-0-2) 2	Embedded Systems and Design Lab(0-0-4)2	L	T	P		Total
I	C-1-1 Portable electronics product design and development (DC) (3-1-0) 4	C-1-2 Device modeling and circuit simulation (DC) (3-0-0) 3	C-1-3 Embedded system design (DC) (3-0-0) 3	C-1-4 DE 1 (3-0-0) 3	C-1-5 OE-1 (3-0-0) 3	Technical Presentation (1-0-2) 2	Embedded Systems and Design Lab(0-0-4)2	16	1	6	23	20
II	C-2-1 EMI & EMC of electrical equipments (DC) (3-0-0)3	C-2-2 Thermal energy management and conservation (DC) (3-0-0) 3	C-2-3 VLSI Design (DC) (3-0-0) 3	C-2-4 DE 2 (3-0-0) 3	C-2-5 DE 3 (3-0-0) 3	Term Paper (0-2-0) 2	E-CAD Lab (0-0-4)2	16	0	6	22	19
III	Seminar (0-0-4)2	Project (0-0-8)4	Dissertation-I (0-0-20)10					0	0	28	28	16
IV	Dissertation-II (0-0-32)16							0	0	32	32	16

C-X-Y (Course-Semester-Paper No.); L-lecture, T-tutorial, P-practical

Total Credits -71

Dr. P. V. S. Rao
Dr. K. S. Ramesh
Dr. K. S. Ramesh
Dr. K. S. Ramesh

LIST OF DEPARTMENTAL ELECTIVES/OPEN ELECTIVES

- Financial Accounting
- Energy modeling ,economics and project management
- Electronics and electrical measurement and instrumentation
- Contracts Laws & regulations
- compact and broadband microstrip antennas design
- consumer electronics

LIST OF OPEN ELECTIVES







- Yield and Reliability Engineering
- Digital signal processing
- Advanced material for thermal management of electronics packaging

Approved
Bos Recommendations
11.6.15
[Signature]

[Signature]

SUMMARY SHEET







Teaching Department Electronics and Communication Engineering
School School of Engineering and Technology
Name of Course M.Tech. In Electronics and Communication
Specialization in-
Microwave Technology
VLSI Technology
Digital Communication
Electronic System Designing and Management
Optical Communication
Digital Signal Processing
Duration Two (02) years
Total No. of Credits 71
Date of meetings of BOS June, 11, 2015
Member of BOS

1. Prof. Abdhesh Singh (HoD), ECE 
2. Prof. R. M. Mehra
3. Dr. Pallavi Gupta 
4. Mr. Sandeep Singh 
5. Ms Shaheen Naz 
6. Ms. Rashmi Priyadarshini 
7. Mr. Yogendra Singh Gill 




11/6/15

SUMMARY SHEET

Teaching Department Electronics and Communication Engineering
School School of Engineering and Technology
Name of Course M.Tech. In Electronics and Communication
Specialization in-
Microwave Technology
VLSI Technology
Digital Communication
Electronic System Designing and Management
Optical Communication
Digital Signal Processing
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 5. Ms Shaheen Naz 
 6. Ms. Rashmi Priyadarshini 
 7. Mr. Yogendra Singh Gill 


11/6/15


Chopra...

Department of Electronic and Communication Engineering
School of Engineering and Technology
Sharda University

LIST OF PROPOSED MEMBERS OF BOARD OF STUDIES (BOS)

A. From University/IIT

✓ 1. Prof. Raj Senani
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✓ 2. Prof. Avinashi Kapoor
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E-mail: avinashi_kapoor@yahoo.com
Mobile: 09350571397

B. From Industry

✓ 3. Dr. Amitava Majumdar
General Manager - Engineering and R&D
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Mobile: 09810834211

C. From Department of Electronic and Communication Engineering
School of Engineering and Technology
Sharda University

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5. Prof. R. M. Mehra
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Mobile: 9971271374

6. Dr. Pallavi Gupta (Associate Professor)
pallavi.gupta2@sharda.ac.in
Mobile: 8800998813

Minutes of Meeting of BOS of ECE - M.Tech (ESDM)

The following changes in Course Structure & syllabi of the following M.Tech (Electronic System Designing and Management).

For Batch 2018-2020

1. Few changes in subject of Department Elective (DE) and Open Elective list.

LIST OF DEPARTMENTAL ELECTIVES/OPEN ELECTIVES

LIST OF DEPARTMENTAL ELECTIVES

1. Renewable Energy
2. Device Modeling and Circuit Simulation
3. Systems Engineering
4. Virtual Instrumentation
5. Discrete Time Signal Processing
6. MEMS, Smart Sensors and WSN
7. PLC,DCS and SCADA
8. Industrial Network Protocols and IoT
9. Intelligent Actuators AND and Mechatronics
10. Management courses

LIST OF OPEN ELECTIVES

1. MEMS Technology and application
2. Yield and Reliability Engineering
3. Advanced DSP
4. Entrepreneurship
5. Telemetry and SCADA
6. Management courses

Sem
Above
method

Shekhar Singh
24/5

Edrup
05/06/18

5.6.18

Pring