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Mobile: 9911450973

Submitted for consideration and the approval of Dean, SET/ Vice-Chancellor, SU

AK Singh
Dr. A. K. Singh

~~Dean SET~~

May like to approve

AK
10/11/16

~~Vice-Chancellor~~

*Please add me
more person in
Category B.*

Dr. A. K. Singh

SUMMARY SHEET

Department: Electronics and Communication Engineering
School: School of Engineering and Technology
Name of the Course: Ongoing B.Tech. in ECE (Term-VII and Term-VIII)
Total No. of Credits: 58
Date of meeting of BOS: July 12, 2012
Status: Approved by BOS

Members of BOS:	1. Prof. R. M. Mehra, HOD, ECE	Chairman
	2. Prof. H. M. Gupta, IIT Delhi	External Member
	3. Prof. Avinashi Kapoor, University of Delhi	External Member
	4. Dr. Rajeev Srivastava, ST Microelectronics	External Member
	5. Dr. Amitava Majumdar, Moser Baer India	External Member
	6. Mr. Sandeep Singh, Coordinator, ECE	Internal member
	7. Ms Rashmi Priyadarshini, ECE	Internal member
	8. Ms Noor e Zahra, ECE	Internal member

Date of Meeting of Faculty Board: July 18, 2012
Status: Approved by BOS and Faculty Board

(Signature)
Amitava
Noor

Minutes of meeting of BOS of ECE held on June 11, 2015

A meeting of the Board of studies was held on 11th June 2015 at Block-I 402, ECE Boardroom at 2:00 pm.

The following members were present.

1. Prof. Raj Senani, NSIT
2. Prof. Avinashi Kapoor, University of Delhi
3. Dr. Amitava Majumdar, Moser Baer India
4. Prof. R. M. Mehra
5. Prof. Abdhesh Singh (HoD), ECE
6. Dr. Pallavi Gupta
7. Mr. Sandeep Singh
8. Ms Shaheen Naz

The course structure & syllabi of the following courses were considered.

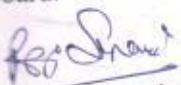
B.Tech. In Electronics and Communication

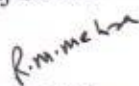
M.Tech.in ECE with Specialization:

1. Microwave Technology
2. VLSI Technology
3. Digital Communication (NEW)
4. Electronic System Design and Management (NEW)
5. Optical Communication (NEW)
6. Digital Signal Processing (NEW)


All the course structures and syllabi were discussed and approved with minor modification in the syllabus of the subject EMFT. One unit of transmission lines be included. The change has been incorporated.

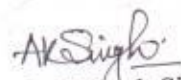
It is recommended that the details of the above approved courses be placed before the faculty Board.

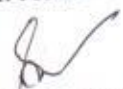

Prof. Raj Senani



Prof. R. M. Mehra

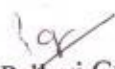

Mr. Sandeep Singh


Prof. Avinashi Kapoor


Prof. Abdhesh Singh


Ms Shaheen Naz


Dr. Amitava Majumdar


Dr. Pallavi Gupta

B.Tech.
in
Electronics and Communication Engineering
COURSE STRUCTURE & SYLLABI

(2nd to 4th Year With effect from academic session 2015-16)



Department of Electronics and Communication Engineering
School of Engineering and Technology
SHARDA UNIVERSITY

B.Tech First Year Course Structure for 2015

Sem.	COURSE STRUCTURE						Courses	L	T	P	Weekly Contact	Credit
	Physics-2 PHY-112 (3-1-2)5	Logic Building in problem solving CSE-105 (3-1-2)5	Mathematics-1 MTH-112 (3-2-0)4	Env.Sc. EVS-103 (2-0-0)2	Manufacturing Science EEE 154 (0-0-4)2	Functional English-I ENG-102 (2-0-2)3						
I							6	4	10		27	21
II	Chemistry CHY-105 (3-1-2)5	Programming using 'C' CSE-106 (3-0-2)4	Mathematics-2 MTH-113 (3-2-0)4	Physics-4 PHY-114 (3-1-0)4	PEEE EEE-110 (3-1-2)5	Functional English-II ENG-103 (2-0-2)3	6	5	8		30	25
											Total	46

B. Tech ECE- Session 2012-2016

Sem.	Courses (L-T-P)										Courses	Contact Hours				Credits
	III	IV	V	VI	Summer Internship (ECE334)					VII		VII I	L	T	P	
III	Analog Circuits (ECE231)(DC) (3-1-2) 5	Digital Electronics- (ECE232)(DC) (3-1-2) 5	Mathematics-3 (Engg Maths) (MTH217) (3-1-0) 4	Managerial Economics (HMM206) (3-0-0) 3	Signals and Systems (ECE233) (DC) (3-1-0) 4	Term Paper (ECE281) (0-0-2) 1	6	15	4	6	24	22				
IV	Digital Electronics- II (DC) (ECE234) (3-1-0) 4	Network Analysis and Synthesis (DC) (ECE237) (3-1-2) 5	EMFT (DC) (ECE235) (3-1-0) 4	Communication Practices (ENG201) (2-0-2) 3	Microprocessors and interfacing (ECE236)(DC) (3-1-2) 5	Term Paper (ECE282) (0-0-2) 1	7	17	4	10	30	26				
V	Linear Integrated Circuit (ECE331) (DC) (3-1-2) 5	Communication Engg. (ECE332) (DC) (3-1-0) 4	DE I (3-1-2) 5	OE I (4-0-0) 4	DSP (DC) (ECE333) (3-1-2) 5	Term Paper (ECE283) (0-0-2) 1	7	19	4	8	30	27				
VI	Digital Communication (DC) (ECE334) (3-1-2) 5	DEII (3-1-2) 5	Microcontroller s and Applications (DC) (ECE335) (3-1-2) 5	OE II (4-0-0) 4	Professional Skills Enhancement t(ENG301) (2-0-2) 3	Term Paper (ECE284) (0-0-2) 1	6	15	3	10	27	23				
VII	CMOS VLSI Design (DC) (ECE431) (3-1-2) 5	DE III (3-0-0) 3	DE IV (3-0-2) 4	OE III (4-0-0) 4	Human Resource Management (HMM405) (3-0-0) 3	Project 2 (ECE491)	0	0	0	0	0	02				
VII I	DE V (3-0-0)3	OE IV (4-0-0) 4	DE VI (3-0-0) 3	Project 8 (ECE492)	Writing For Technical For Purposes (ENG401)(3-0-0)3		5	13	0	0	13	21				
TOTAL											12+ 37= 49	27+95 =122	3+ 16= 19	18+3 8=56 96	51+ 145=1 183	

B. Tech Course Structure for 2015-16

Sem.	Courses (L-T-P)							Courses	Contact Hours				Credits	
	Analog Circuits (DC) (3-1-2) 5 NEW <i>ECE-221</i>	Digital Electronics (DC) (3-1-2) 5 <i>ECC-222</i>	Networks and Systems (3-1-2) 5 <i>ECE-223</i>	Mathematics-3 Engg Maths MTH217 /Introductory Mathematics* MTH216 (3-1-0) 4	Communication Practices ENG202 (2-0-2) 3	Term Paper ECP280 (0-0-2) 1	5		L	T	P	Total		
III														
IV	Analog and Digital Circuits (DC) (3-1-2) 5 NEW	Microprocessor and Interfacing ECE236 (DC) (3-0-2) 4	EMFT (DC) ECE235 (3-1-0) 4	Concepts of OOPS programming using Java CMP202 (3-1-2) 5	Economics for Engineers HMM206 (3-0-0) 3	Term Paper ECP281 (0-0-2) 1	5	15	3	8	26		22	
V	Linear Integrated Circuit (DC) ECE331 (3-1-2) 5	Communication Engg. (DC) ECE332 (3-1-2) 5	Digital Signal Processing (DC) ECE333 (3-1-2) 5	Microwave & Radar Engineering (DC) (3-0-2) 5 NEW	Professional Skills Enhancement (2-0-2) 4 ENG301	Term Paper ECP380 (0-0-2) 1	5	15	3	12	30		24	
VI	Digital Communication (DC) ECE334 (3-1-2) 5	Microcontrollers and Applications (DC) ECE 335 (3-0-2) 4	Control System (3-1-2) 5 (DC) ECE341 NEW	Management for Engineers HMM303 (3-0-0) 3	DEI/Math 3* (3-1-0) 4	Term Paper ECP381 (0-0-2) 1	5	15	3	8	26		22	
Summer Internship ECE394 (0-0-4) 2													2	
VII	CMOS VLSI Design (DC) ECE431 (3-1-2) 5	Advanced Written Communication ENGXXX (3-0-0) 3	Project (0-0-4) 2 ECE490	OE I (3-0-0) 3	DE II (3-1-0) 4	DE III (3-1-2) 5	6	15	3	8	26		22	
VIII	DE IV (3-1-0) 4	OE II (3-0-0) 3	DE V (3-1-0) 4	Project ECE491 (0-0-16) 8	Project Management HMNXXX (3-0-0) 3		5	12	2	16	30		22	
TOTAL							12+ 41= 51	27+ 86= 113	3+1 8= 21	18+ 62= 80	51+ 154= 205	46+ 137= 183		

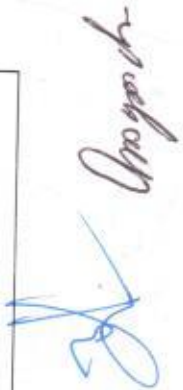
*For Diploma Holders only

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Bas recommends - [Signature]
Approved

Some Modifications/Changes are as Follows for B. Tech 2015

Present B. Tech Courses	Modifications/Changes for B. Tech Courses 2015
Signals and Systems	Networks and Systems ECE 237 III Semester
Networks analysis and Synthesis	
Analog Circuits-I	Analog Circuits Syllabus changed in III semester
Digital electronic II	Merged in Analog and Digital Circuits (New Subject) IV Semester
Managerial Economics	Renamed Economics For Engineers, shifted from III to IV Semester
Communications Practices	Shifted from IV to III Semester
Maths-3	Introductory Mathematics for Diploma Holders and maths-3 for all others in III Semester
Concepts of OOPS programming using C++ CMP202 in IV Semester	Renamed Concepts of OOPS programming using JAVA CMP202 in IV Semester
Microwave & radar Engineering is in departmental elective	Microwave & Radar Engineering included in 5 th semester
Professional skills enhancement	Professional skills enhancement shifted to V semester to VI semester
Organizational behavior	Organizational behavior renamed as Management for engineers now in VI semester shifted from V semester
Control system is in DE	Control system included in as core course in VI semester
Mathematics- 3	Mathematics- 3 is a Departmental Elective for diploma holders (DE-1)
Human resource management	Renamed as project management shifted to VIII semester from VII semester
Writing for technical purposes	Renamed as Advanced Written semester and shifted to VII semester from VIII semester
Department electives are six	Reduced to five
Open elective are four	Reduced to two of each three credits

P.S. Suresh
11/6/15
bos recommends



Departmental electives

S.No	School	Discipline	Degree	Term	Course title	L	T	P	Credits	Compulsory or Elective
1	SET	ECE	B.Tech.	6	Mixed Signal CMOS VLSI Design	3	1	0	4	Departmental Elective-1
2	SET	ECE	B.Tech.	6	Real Time operating Systems	3	1	0	4	
3	SET	ECE	B.Tech.	6	Antennas & Wave Propagation	3	1	0	4	
4	SET	ECE	B.Tech.	7	VHDL Design	3	1	0	4	
5	SET	ECE	B.Tech.	7	Programming and Customizing the AVR Microcontroller	3	1	0	4	Departmental Elective-2
6	SET	ECE	B.Tech.	7	RF & Microstrip Antennas	3	1	0	4	Departmental Elective-3
7	SET	ECE	B.Tech.	7	Optical Communications	3	1	2	5	
8	SET	ECE	B.Tech.	7	Image Processing	3	1	2	5	
9	SET	ECE	B.Tech.	7	ASIC/FPGA Design	3	1	2	5	
10	SET	ECE	B.Tech.	7	Advanced Communication System	3	1	2	5	
11	SET	ECE	B.Tech.	8	Telecommunication Switching	3	1	0	4	Departmental Elective-4
12	SET	ECE	B.Tech.	8	Satellite Communications	3	1	0	4	
13	SET	ECE	B.Tech.	8	Data Communication Network	3	1	0	4	
14	SET	ECE	B.Tech.	8	Wireless & Mobile Communication	3	1	0	4	
15	SET	ECE	B.Tech.	8	CMOS: Circuit Design, Layout and Simulation	3	1	0	4	Departmental Elective-5
16	SET	ECE	B.Tech.	8	Embedded System	3	1	0	4	
17	SET	ECE	B.Tech.	8	CAD for Patch Antennas Design	3	1	0	4	

Following new subjects has been added in departmental electives

S.No	School	Discipline	Degree	Term	Course code	Course title	L	T	P	Credits	Compulsory or Elective
1	SET	ECE	B.Tech.	5	NEW	CMOS: Circuit Design, Layout and Simulation	3	0	0	3	Departmental Elective-1
2	SET	ECE	B.Tech.	6	NEW	RF and Microstrip patch Antenna	3	0	0	3	Departmental Elective-2
3	SET	ECE	B.Tech.	7	NEW	Real Time operating Systems	3	0	0	3	Departmental Elective-3
4	SET	ECE	B.Tech.	7	NEW	CAD of Patch Antenna for Wireless applications	3	0	2	4	Departmental Elective-4
5	SET	ECE	B.Tech.	8	NEW	Programming and Customizing the AVR Microcontroller	3	0	0	3	Departmental Elective-5
6	SET	ECE	B.Tech.	8	NEW	Mixed Signal CMOS VLSI Design	3	0	0	3	
7	SET	ECE	B.Tech.	8	NEW	Embedded RF System and high frequency Signal Processing	3	0	0	3	

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11/11/15

Open electives

S.No	School	Discipline	Degree	Term	Course code	Course title	L	T	P	Credits	Compulsory or Elective
1	SET	ECE	B.Tech.	7	ECE438	Photovoltaic Devices	3	0	0	3	Open Elective-1
2	SET	ECE	B.Tech.	7	ECE471	Consumer Electronics	3	0	0	3	
3	SET	ECE	B.Tech.	7	ECE337	Digital Logic Design	3	0	0	3	
6	SET	ECE	B.Tech.	8	ECE467	Bio-medical Instrumentation	3	0	0	3	Open Elective-2
8	SET	ECE	B.Tech.	8	ECE349	Transducers & Sensor	3	0	0	3	
9	SET	ECE	B.Tech.	8	ECE462	Smart Sensors And Sensors Networking	3	0	0	3	
10	SET	ECE	B.Tech.	8	ECE239	Electrical And Electronic Measurement And Instrumentation	3	0	0	3	

Following new subjects has been added in Open electives

S.No	School	Discipline	Degree	Term	Course code	Course title	L	T	P	Credits	Compulsory or Elective
1	SET	ECE	B.Tech.	7	NEW	Bio-Instrumentation	3	0	0	3	Open Elective-1
2	SET	ECE	B.Tech.	7	NEW	User Interface Design	3	0	0	3	
3	SET	ECE	B.Tech.	8	NEW	Mobile Computing	3	0	0	3	Open Elective-2
4	SET	ECE	B.Tech.	8	NEW	Nano-Electronics	3	0	0	3	

Open your choice

new recommendations

BoS

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M.Tech.

in

*Electronics and Communication Engineering
(With Specialization in Digital Communication)*

COURSE STRUCTURE & SYLLABI

(With effect from academic session 2015-17)



*Department of Electronics and Communication
Engineering
School of Engineering and Technology
SHARDA UNIVERSITY*

Department Electives

School	Discipline	Degree	Term	Course code	Course title	L	T	P	Credits	Compulsory or Elective
SET	ECE	M.Tech.		ECE 665	Neural Networks	3	0	0	3	Departmental Elective-1
SET	ECE	M.Tech.		ECE 686	Microwave Communication	3	0	0	3	Departmental Elective-1
SET	ECE	M.Tech.		ECE 687	Design of Communication Networks	3	0	0	3	Departmental Elective-2
SET	ECE	M.Tech.		ECE 667	Data Communication	3	0	0	3	Departmental Elective-2
SET	ECE	M.Tech.		ECE 669	Secure Communication	3	0	0	3	Departmental Elective-3
SET	ECE	M.Tech.		ECE 688	Pattern Recognition and Analysis	3	0	0	3	Departmental Elective-3
SET	ECE	M.Tech.		ECE 689	Cryptography	3	0	0	3	Departmental Elective-3

Open Electives Offered by Department

School	Discipline	Degree	Term	Course code	Course title	L	T	P	Credits	Compulsory or Elective
SET	ECE	M.Tech.		ECE 777	Cellular and Mobile Communication	3	0	0	3	Open Elective-1
SET	ECE	M.Tech.		ECE 619	Nanoelectronics	3	0	0	3	Open Elective-1
SET	ECE	M.Tech.		ECE 640	RF and Micro-MEMS	3	0	0	3	Open Elective-1

Course Structure

M.Tech. in ECE with specialization in Digital Communication

(Degree by Course work and Dissertation)

		Courses (L-T-P)						Lecture Courses				Contact Hr/Week				Credits
								L	T	P	Total	L	T	P	Total	
Computational Methods for Comm. ECE683 (DC) (3-1-0) 4	Discrete time signal processing ECE684 (3-0-0) 3	Advanced Digital Comm. ECE685(3-0-0) 3	DE 1 (3-0-0) 3	DE-2 (3-0-0) 3	ENP601 Technical Presentation (1-0-2) 2 (004)2	Digital Signal Processing Lab ECP684 (0-0-4)2	6	16	1	6	23	20				
Information Theory & Coding ECE771(DC) (3-0-0)3	Detection & Estimation Theory ECE772 (3-0-0) 3	Modern Telecom Switching Systems ECE773 (3-0-0) 3	OE 1 (3-0-0) 3	DE 3 (3-0-0) 3	Term Paper ECP681 (0-2-0) 2	Communication Lab ECP685 (0-0-4)2	6	16	0	6	22	19				
Seminar ECE781 (0-0-4)2	Project ECE782 (0-0-8)4	Dissertation-I ECE783 (0-0-20)10					0	0	0	28	28	16				
Dissertation-II ECE784 (0-0-32)16							0	0	0	32	32	16				
C-X-Y (Course-Semester-Paper No.); L-lecture, T-tutorial, P-practical												Total Credits -71				

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Course Structure

M.Tech. in ECE with specialization in Digital Communication

SEMESTER	Courses (L-T-P)						Contact Hr/Week					
							Lecture Courses				Total	
	L	T	P									
I	C-1-1 Computational Methods for Comm (DC) (3-1-0) 4	C-1- Discrete time signal processing (3-0-0) 3	C-1-3 Advanced Digital Comm. (3-0-0) 3	C-1-4 DE 1 (3-0-0) 3	C-1-5 OE-1 (3-0-0) 3	Technical Presentation (1-0-2) 2	Digital Signal Processing Lab (0-0-4)2	6	16	1	6	23
II	C-2-1 Information Theory & Coding (DC) (3-0-0)3	C-2 Detection & Estimation Theory (3-0-0) 3	C-2-3 Modern Telecom Switching Systems (3-0-0) 3 DE 3	C-2-4 DE 2 (3-0-0) 3	C-2-5 DE 3 (3-0-0) 3	Term Paper (0-2-0) 2	Communication Lab Lab (0-0-4)2	6	16	0	6	22
III	Seminar (0-0-4)2	Project (0-0-8)4	Dissertation-I (0-0-20)10					0	0	0	28	28
IV	Dissertation-II (0-0-32)16							0	0	0	32	32

C-X-Y (Course-Semester-Paper No.): L-lecture, T-tutorial, P-practical

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 BOS recommends

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Total Credits -

LIST OF DEPARTMENTAL ELECTIVES

- Neural Networks
- Microwave Communication
- Secure Communication
- Design of Communication Networks
- Pattern Recognition and Analysis
- Data Communication
- Cryptography

LIST OF OPEN ELECTIVES

- Wireless and Mobile Communication
- Nano-electronics
- RF and Micro-MEMS

Recommender's *[Signature]* Approved *[Signature]*
Date: 16/11/15

Course Structure

M.Tech. in ECE with Specialization in Electronic System Design and Management

SEMESTER	Courses (L-T-P)						Contact Hr/Week				Credits	
	C-1-1	C-1-2	C-1-3	C-1-4	C-1-5	Technical Presentation (1-0-2) 2	Embedded Systems and Design Lab(0-0-4)2	L	T	P		Total
I	C-1-1 Portable electronics product design and development (DC) (3-1-0) 4	C-1-2 Device modeling and circuit simulation (DC) (3-0-0) 3	C-1-3 Embedded system design (DC) (3-0-0) 3	C-1-4 DE 1 (3-0-0) 3	C-1-5 OE-1 (3-0-0) 3	Technical Presentation (1-0-2) 2	Embedded Systems and Design Lab(0-0-4)2	16	1	6	23	20
II	C-2-1 EMI & EMC of electrical equipments (DC) (3-0-0)3	C-2-2 Thermal energy management and conservation (DC) (3-0-0) 3	C-2-3 VLSI Design (DC) (3-0-0) 3	C-2-4 DE 2 (3-0-0) 3	C-2-5 DE 3 (3-0-0) 3	Term Paper (0-2-0) 2	E-CAD Lab (0-0-4)2	16	0	6	22	19
III	Seminar (0-0-4)2	Project (0-0-8)4	Dissertation-I (0-0-20)10					0	0	28	28	16
IV	Dissertation-II (0-0-32)16							0	0	32	32	16

C-X-Y (Course-Semester-Paper No.); L-lecture, T-tutorial, P-practical

Total Credits -71

Dr. P. V. S. Rao
Dr. K. S. Ramesh
Dr. K. S. Ramesh
Dr. K. S. Ramesh

LIST OF DEPARTMENTAL ELECTIVES/OPEN ELECTIVES

- Financial Accounting
- Energy modeling ,economics and project managment
- Electronics and electrical measurement and instrumentation
- Contracts Laws & regulations
- compact and broadband microstrip antennas design
- consumer electronics

LIST OF OPEN ELECTIVES

- Yield and Reliability Engineering
- Digital signal processing
- Advanced material for thermal management of electronics packaging

Approved
Bos Recommendations
11.6.15
[Signature]

[Signature]



Rashmi Priyadarshini (Suset Associate Professor) <rashmi.priyadarshini@sharda.ac

Fwd: REQUEST/BOS/Meeting

2 messages

R.M. Mehra (Suset Professor Emeritus) <rm.mehra@sharda.ac.in>

To: Rashmi Priyadarshini <rashmi.priyadarshini@sharda.ac.in>

Tue, Dec 4, 2018 at 1:39

R. M. Mehra
9971271374

----- Forwarded message -----

From: **Dr. R. M. Mehra** <rm.mehra@sharda.ac.in>

Date: Mon, 2 Jul 2012 at 17:07

Subject: REQUEST/BOS/Meeting

To: <jp_sub@yahoo.com>, <jpsrm@mech.iitd.ac.in>

Cc: Ramesh Chander Sachdeva <rc.sachdeva@sharda.ac.in>, Dr. Gopal P. Sinha <gopal.sinha@sharda.ac.in>

To,

Prof. J.P. Subrahmanyam**Head, Dept. of Mechanical Engineering****IIT, Delhi****New Delhi**

E-mail: jp_sub@yahoo.com, jpsrm@mech.iitd.ac.in

Subject: - Invitation for the combined meeting of Board of Studies of School of
Engineering & Technology.

Dear Prof. J.P. Subrahmanyam

It is our privilege to invite you to attend the meeting of Combined Board of Studies (BOS) for the School of Engineering & Technology, Sharda University on 12th July 2012 at 10 am in the T&P Board Room (Ground Floor, PGDM Building). The whole day meeting will be in two sessions with lunch break. The principal agenda will be

1. Course structure and syllabi of M.Tech. programme.
2. Approval of the syllabi of 7th & 8th terms of B.Tech. programme already running.

We look forward to your kind presence at 10:00 am on 12th July 2012 at T&P conference room. A line in confirmation will be highly appreciated.

Course Structure
M.Tech. in ECE with specialization in VLSI-2012-15
 (Degree by Course work and Dissertation)

SEMESTER	Courses (L-T-P)										Contact Hr/Week			Total
	ECE 611 Advanced Computer Architecture (DC) (3-0-0) 43	ECE 612 Advanced Digital Design using HDL (DC) (3-0-0) 4	ECE 618 Mobile and Wireless Communication (DC) (3-0-0) 3	DE-1 (3-0-0) 3	DE-2 (3-0-0) 3	Language (0-0-4) 2 ENP 601	Digital s/y (0-0-4) 2 Design lab. ECP-G51	Lecture Courses	L	T	P			
I								6	16	1	6		23	20
II	ECE 615 CMOS Digital VLSI Design, Testing and Verification (DC) (3-0-0) 3	ECE 616 Advanced VLSI Design (3-0-0) 3	ECE 617 Mixed Signal CMOS VLSI Design (3-0-0) 3	OE 1 (3-0-0) 3	DE 3 (3-0-0) 3	Technical Communication (1-0-2) 2	(0-0-4) 2 Electronics CAD LAB ECP-G52	6	16	0	6		22	19
III	Seminar (0-0-4) 2 ECE 691	Project (0-0-8) 4 ECE-692	Dissertation-I (0-0-20) 10 ECE 693					0	0	0	28		28	16
IV	Dissertation-II (0-0-32) 16 ECE 694							0	0	0	32		32	16
C-X-Y (Course-Semester-Paper No.); L-lecture, T-tutorial, P-practical												Total Credits -71		

Department Electives										
School	Discipline	Degree	Term	Course code	Course title	L	T	P	Credits	Compulsory or Elective
SET	ECE	M.Tech.	1	ECE613	ANALOG IC DESIGN ✓	3	0	0	3	Department Elective-1
SET	ECE	M.Tech.	1	ECE619	Nanoelectronics	3	0	0	3	Department Elective-1
SET	ECE	M.Tech.	1	ECE620	Biomedical Electronics	3	0	0	3	Department Elective-1
SET	ECE	M.Tech.	1	ECE614	Device Modelling and Circuit Simulation ✓	3	0	0	3	Department Elective-2
SET	ECE	M.Tech.	1	ECE625	MEMS and IC Integration	3	0	0	3	Department Elective-2
SET	ECE	M.Tech.	1	ECE626	Advanced Digital Communications System	3	0	0	3	Department Elective-2
SET	ECE	M.Tech.	2	ECE621	Yield and Reliability Engineering	3	0	0	3	Department Elective-2
SET	ECE	M.Tech.	2	ECE623	Advanced Digital Signal Processing	3	0	0	3	Department Elective-3

Open Electives Offered by Department										
School	Discipline	Degree	Term	Course code	Course title	L	T	P	Credits	Compulsory or Elective
SET	ECE	M.Tech.	2	ECE622	ASIC/PGA DESIGN	3	0	0	3	Open Elective-1
SET	ECE	M.Tech.	2	ECE624	Embedded Systems	3	0	0	3	Open Elective-1
SET	ECE	M.Tech.	2	ECE644	Remote Sensing	3	0	0	3	Open Elective-1

M. Tech. VLSI/Technology

4 messages

Dr. R. M. Mehra <rm.mehra@sharda.ac.in>

Fri, Jun 21, 2013 at 4:18 PM

To: "Prof. Avinashi Kapoor" <avinashi_kapoor@yahoo.com>, "Dr. Amitava Majumdar" <a.majumdar@moserbaer.in>, "Dr. Rajeev Srivastava" <rajeev.rajeevsrivastava@gmail.com>, "Dr. B. K. Panigarhi" <bkpanigrahi@ee.iitd.ac.in>

Dear BOS Member/Expert

It is to inform you that as a matter of policy the minimum credit for M. Tech. courses has been raised from 64 to 70.

Keeping in view we have made few modifications in the structure and syllabi of the M.Tech. in VLSI technology which was approved by you last year.


Kindly examine it and send your approval.


Thanking you

With regards

R M Mehra

2 attachments

 **Modified Course Structure M.Tech.xlsx**
22K

 **M.Tech. ECE with VLSI modified syllabus.doc**
131K

Prof. Avinashi Kapoor <avinashi_kapoor@yahoo.com>

Sat, Jun 22, 2013 at 10:08 AM

Reply-To: "Prof. Avinashi Kapoor" <james.mott_esq@gmail.com>

To: "Dr. R. M. Mehra" <rm.mehra@sharda.ac.in>

Dear Prof.Mehra,

I agree to the new proposal.

with regards,

Prof. Avinashi Kapoor
Jt. Dean Student Welfare
South Campus, University of Delhi
New Delhi, INDIA
Mob. +91-9350571397
Off. +91-11-24116178, 24110440

*R.M.Meh
22.6.2013*

From: Dr. R. M. Mehra <rm.mehra@sharda.ac.in>

To: Prof. Avinashi Kapoor <avinashi_kapoor@yahoo.com>; Dr. Amitava Majumdar

<a.majumdar@moserbaer.in>; Dr. Rajeev Srivastava <rajeev.rajeevsrivastava@gmail.com>; Dr. B. K. Panigarhi <bkpanigrahi@ee.iitd.ac.in>

Sent: Friday, 21 June 2013 4:18 PM

[Quoted text hidden]

::DISCLAIMER::

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Before opening any email and/or attachments, please check for viruses and other defects.

a.majumdar <a.majumdar@moserbaer.in>
To: "Dr. R. M. Mehra" <rm.mehra@sharda.ac.in>

Sat, Jun 22, 2013 at 11:54 AM

Dear Sir,

I approve the modified syllabi of M.Tech. in VLSI technology .

With regards,

a.majumdar

*R.M. Mehra
22.6.2013*

Dr. Amitava Majumdar | Head & Associate Vice President | Engineering and R&D

Moser Baer India limited

66, Udyog Vihar , Greter Noida - 201306

Phone : 0120-4386342(D)

E.mail : a.majumdar@moserbaer.net

www.moserbaer.in

From: Dr. R. M. Mehra [mailto:rm.mehra@sharda.ac.in]

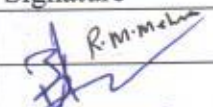
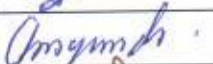
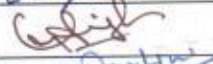

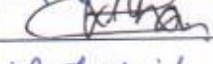
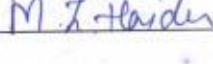

Sent: Friday, June 21, 2013 4:18 PM

Department of Electronic and Communication Engineering
School of Engineering and Technology
Sharda University

Board of Studies

Date: 18/06/2013

Attendance Sheet

S. No.	Name		Signature
1.	Prof. R. M. Mehra, HoD, ECE	Chairman	
2.	Prof. Avinashi Kapoor, DU	External Expert	
3.	Dr. Amitava Majumdar, Moser Baer India	External Expert	
4.	Mr. Ghanshyam Singh, ECE	Internal member	
5.	Ms Rashmi Priyadarshini, ECE	Internal member	
6.	Mr. C. Mohan, EIE	Internal member	
7.	Mr. Zeeshan Haider, EIE	Internal member	

Course Structure
M.Tech. in ECE with specialization in VLSI Technology, Sharda University
(By Course Work)

SEMESTER	Courses (L-T-P) Credits										Contact Hr/Week			Credits
	C-1-1 Advanced Computer Architecture (DC)(3-0-0) 3	C-1-2 Advance Digital design using HDL (DC)(3-1-0)4	C-1-3 Mobile and Wireless Communication (DC) (3-0-0)3	C-1-4 DE I (3-0-0)3	C-1-5 DE II (3-0-0)3	Foreign Language (1-0-2) 2	Modelling Lab (0-0-4)2	Lecture Courses	L	T	P	Total		
I									6	16	1	6	23	20
II	C-2-1 Mixed Signal CMOS VLSI Design (DC) (3-0-0)3	C-2-2 CMOS Digital VLSI Design, Testing and verification (DC) (3-0-0)3	C-2-3 Advanced VLSI Design (DC) (3-0-0)3	C-2-4 DE-III (3-0-0)3	C-2-5 OE-I (3-0-0) 3	Technical Presentation (1-0-2) 2	Electronics CAD Lab (0-0-4)2	6	16	0	6	22	19	
III	Seminar-I (0-0-4)2	Project-1 (0-0-8)4	DE-IV (3-0-0)3	DE-V (3-0-0)3	OE-II (3-0-0) 3			3	9	0	12	21	15	
IV	Seminar--II (0-0-4)2	Project-2 (0-0-12)6	DE-VI (3-0-0)3	DE-VII (3-0-0)3	OE-III (3-0-0) 3			3	9	0	16	25	17	
												Total Credits - 71		

C-X-Y (Course-Semester-Paper No.); L-lecture, T-tutorial, P-practical

Approved by
26/02/14

Approved by
26/02/14

26/02/14

26.2.2014

26.2.14

26/02/14

LIST OF DEPARTMENTAL CORE

1. Advanced Computer Architecture
2. Advance Digital design using HDL
3. CMOS Digital VLSI Design, Testing and verification
4. Mobile and Wireless Communication
5. Advanced VLSI Design
6. VLSI Signal Processing
7. Device Modeling and Circuit Simulation
8. Analog IC design

LIST OF DEPARTMENTAL ELECTIVES

1. Nanoelectronics
2. Biomedical Electronics
3. Yield and Reliability Engineering
4. Analog IC Design
5. MEMS and IC Integration
6. Advanced Digital Communication System
7. ASIC/FPGA Design
8. Device Modeling and Circuit simulation
9. Advanced DSP
10. Embedded Systems
11. MICROWIND DSCH and its applications
12. ATLAS and its applications

H. N. M. S. 26/12/2017.
H. N. M. S. 26/12/2017.

Deep 26/2/17

Prof. J. S. 26/12/17
H. N. M. S. 26/12/17

Nov 26/12/17

26/12/17

26/12/17

Course Structure
M.Tech. in ECE with specialization in VLSI Technology, Sharda University
(By Course Work and Dissertation)

SEMESTER	Lecture Course										Contact Hr/Week			Credits
	C-1-1 Advanced Computer Architecture (DC)(3-0-0)3	C-1-2 Advance Digital design using HDL (DC)(3-1-0)4	C-1-3 Advanced VLSI Design (DC) (3-0-0)3	C-1-4 DE-I (3-0-0)3	C-1-5 DE-II (3-0-0)3	Foreign Language (1-0-2)2	Practical Modelling Lab (0-0-4)2	6	L	T	P	Total		
I													20	
II	C-2-1 Mixed Signal CMOS VLSI Design (DC) (3-0-0)0	C-2-2 CMOS Digital VLSI Design, Testing and verification (DC) (3-0-0)3	C-2-3 Mobile and Wireless Communication (DC) (3-0-0)3	C-2-4 DE-III (3-0-0)3	C-2-5 OE (3-0-0)3	Technical Presentation (1-0-2)2	Practical Electronics CAD Lab (0-0-4)2	6	16	0	6	22	19	
III	Seminar (0-0-4)2	Project (0-0-8)4	Dissertation-I (0-0-20)10					0	0	0	32	32	16	
IV	Dissertation-II (0-0-32)16							0	0	0	32	32	16	
Total Credits – 71														

C-X-Y (Course-Semester-Paper No.); L-lecture, T-tutorial, P-practical

Course Structure
M.Tech. in ECE with specialization in Microwave Technology, Sharda University
(By Course Work)

SEMESTER	Courses (L-T-P) Credits						Lecture Courses			Contact Hr/Week			Credits
	C-1-1	C-1-2	C-1-3	C-1-4	C-1-5	Foreign Language (1-0-2) 2	Microwave Measurements Laboratory (0-0-4) 2	L	T	P	Total		
I	C-1-1 Electromagnetic Field and Transmission Line (DC) (3-1-0) 4	C-1-2 Communication Systems (DC)(3-0-0)3	C-1-3 Microwave Measurement Techniques and Applications (DC) (3-0-0) 3	C-1-4 DE I (3-0-0)3	C-1-5 DE II (3-0-0)3	Foreign Language (1-0-2) 2	Microwave Measurements Laboratory (0-0-4) 2	16	1	6	23	20	
II	C-2-1 MIC and MMIC (DC) (3-0-0)3	C-2-2 Microwave Tubes and Solid state devices (DC) (3-0-0)3	C-2-3 Satellite Communication (DC) (3-0-0)3	C-2-4 DE-III (3-0-0)3	C-2-5 OE-I (3-0-0) 3	Technical Presentation (1-0-2) 2	Microwave Circuits CAD Laboratory (0-0-4)2	16	0	6	22	19	
III	Seminar-I (0-0-4)2	Project-1 (0-0-8)4	DE-IV (3-0-0)3	DE-V (3-0-0)3	OE-II (3-0-0) 3			9	0	12	21	15	
IV	Seminar--II (0-0-4)2	Project-2 (0-0-12)6	DE-VI (3-0-0)3	DE-VII (3-0-0)3	OE-III (3-0-0) 3			9	0	16	25	17	

Total Credits – 71

C-X-Y (Course-Semester-Paper No.): L-lecture, T-tutorial, P-practical

 26/02/2014
 26/02/14
 26/02/14
 26/02/14
 26/02/14
 26/02/14

LIST OF DEPARTMENTAL CORE

1. Electromagnetic Field and Transmission Line
2. Communication Systems
3. Microwave Tubes & Solid State Devices
4. Microwave Measurement Techniques and Applications
5. Satellite Communication
6. MIC and Monolithic Microwave Integrated Circuits (MMIC)
7. Radar systems and signal processing
8. Microwave and Passive Circuits

LIST OF DEPARTMENTAL ELECTIVES

1. Antennas
2. Cavity Measurements
3. RF and Micro MEMS
4. Microwave and passive circuits
5. Mobile and Wireless Communication
6. Photonics and Optical Switching
7. RF System Design for Wireless Applications.
8. Radar systems and signal processing
9. Microwave Active Circuits
10. Computational Electromagnetic
11. Remote Sensing
12. FDTD and its application
13. Plasmonic Structured Devices

M. I. Haider

26/02/14
26/02/14
26/02/14

26/02/14
26/02/14
26/02/14

26/02/2014
26/02/2014
26/02/14
26/02/2014

Course Structure
M.Tech. in ECE with specialization in Microwave Technology, Sharda University
(By Course Work and Dissertation)

SEMESTER	Courses (L-T-P) Credits										Contact Hr/Week			Credits
	C-1-1 Electromagnetic Field and Transmission Line (DC) (3-1-0) 4	C-1-2 Communication Systems (DC) (3-0-0)3	C-1-3 Microwave Measurement Techniques and Applications (3-0-0)3	C-1-4 DE I (3-0-0)3	C-1-5 DE II (3-0-0)3	Foreign Language (1-0-2) 2	Microwave Measurements Laboratory (0-0-4)2	Lecture Courses	L	T	P	Total		
I									6	16	1	6	23	20
II	C-2-1 MIC and MMIC (DC) (3-0-0) 3	C-2-2 Microwave Tubes and Solid state devices (3-0-0)3	C-2-3 Satellite Communication (DC) (3-0-0)3	C-2-4 DE-III (3-0-0)3	C-2-5 OE (3-0-0) 3	Technical Presentation (1-0-2) 2	Microwave Circuits CAD Laboratory (0-0-4)2	6	16	0	6	22	19	
III	Seminar (0-0-4)2	Project (0-0-8)4	Dissertation-I (0-0-20)10					0	0	0	32	32	16	
IV	Dissertation-II (0-0-32)16							0	0	0	32	32	16	
Total Credits – 71														

C-X-Y (Course-Semester-Paper No.); L-lecture, T-tutorial, P-practical

**Department of Electronics and Communication Engineering
School of Engineering and Technology
Sharda University**

Minutes of meeting of Board of Studies (BOS)

A meeting of Board of Studies (BOS) of the department of Electronics and Communication Engineering (ECE) was held on June 16, 2014 at 11:00 am in ECE Board Room (402), SET-I, Sharda University. It was proposed that there will be two new M.Tech. programmes in two stream (i) M.Tech. with course work and dissertation (already existing) and (ii) M.Tech. with course work only

1. M.Tech. in ECE with specialization in Digital Signal Processing.
2. M.Tech. in ECE with specialization in Digital Communication Technology.

The list of members of the BOS is enclosed.

The course structure of the two programmes was presented and explained to the members. The experts critically examined and discussed the proposal. The experts suggested few reference books which have been included in the course syllabus. BOS approved the course structure of the two streams.

Following members were present.

1. Prof. R. M. Mehra
2. Dr. Amitava Majumdar
3. Prof. Avinashi Kapoor
4. Dr. Ghanshyam Singh
5. Mr. Sandeep Singh (AP)
6. Ms. Rashmi Priyadarshini
7. Ms. Noor-e-Zahra (AP)
8. Mr. C. Mohan
9. Mr. Zeeshan Haider

R.M. Mehra

Amitava Majumdar

Avinashi Kapoor

Sandeep Singh
16/06/14

Rashmi Priyadarshini

Noor-e-Zahra
16/06/14

C. Mohan

Zeeshan Haider

M.Tech. in ECE with Specialization in Digital Communication Course Structure

Discipline	Degree	Term	Course code	Course title	L	T	P	Credits	Compulsory or Elective
ECE	M.Tech.	1		Mobile & Wireless Communication	3	1	0	4	Compulsory
ECE	M.Tech.	1		Advance Digital Communication	3	0	0	3	Compulsory
ECE	M.Tech.	1		Computational method for Communication	3	0	0	3	Compulsory
ECE	M.Tech.	1		Communication System Lab	0	0	4	2	Compulsory
ECE	M.Tech.	1			3	0	0	3	Departmental Elective-1
ECE	M.Tech.	1		Detection and Estimation Theory	3	0	0	3	Departmental Elective-2
ECE	M.Tech.	1		Advance Optical Communication	3	0	0	3	Compulsory
ECE	M.Tech.	1		Adaptive Signal Processing	3	0	0	3	Compulsory
ECE	M.Tech.	2		Modelling and Simulation Lab	0	0	4	2	Compulsory
ECE	M.Tech.	2			3	0	0	3	Departmental Elective-3
ECE	M.Tech.	2			3	0	0	3	Open Elective-1
ECE	M.Tech.	3		Seminar	0	0	4	2	Compulsory
ECE	M.Tech.	3		Project	0	0	8	4	Compulsory
ECE	M.Tech.	3		Dissertation-I	0	0	20	10	Compulsory
ECE	M.Tech.	4		Dissertation-II	0	0	32	16	Compulsory

Department Electives

Discipline	Degree	Term	Course code	Course title	L	T	P	Credits	Compulsory or Elective
ECE	M.Tech.	1		Fractional FFT	3	0	0	3	Departmental Elective-1
ECE	M.Tech.	1		Modern Telephone Switching System	3	0	0	3	Departmental Elective-1
ECE	M.Tech.	1		Satellite Communication	3	0	0	3	Departmental Elective-1
ECE	M.Tech.	1		Radar Systems and Signal Processing	3	0	0	3	Departmental Elective-2
ECE	M.Tech.	2		Information Theory and Coding	3	0	0	3	Departmental Elective-2
ECE	M.Tech.	2		RF System design for wireless communications	3	0	0	3	Departmental Elective-2
ECE	M.Tech.	2		Remote Sensing	3	0	0	3	Departmental Elective-3
ECE	M.Tech.	2		Communication Networks	3	0	0	3	Departmental Elective-3
ECE	M.Tech.	2		Wireless Security	3	0	0	3	Departmental Elective-3
ECE	M.Tech.	2		Spread Spectrum and CDMA system	3	0	0	3	Departmental Elective-3

Electives Offered by Department

Discipline	Degree	Term	Course code	Course title	L	T	P	Credits	Compulsory or Elective
ECE	M.Tech.	3		Digital Image Processing	3	0	0	3	Open Elective-1
ECE	M.Tech.	3		Antenna	3	0	0	3	Open Elective-1
ECE	M.Tech.	3		Microwave Active Circuits	3	0	0	3	Open Elective-1
ECE	M.Tech.	3		Wireless Broadband Networks	3	0	0	3	Open Elective-1
ECE	M.Tech.	3		Multimedia Compression Techniques	3	0	0	3	Open Elective-1

Prof. Dr. S. Srinivasan
16/11/14
15/11/14
15/11/14

Course Structure

M.Tech. in ECE with specialization in Digital Communication

	Courses (L-T-P)										Lecture Courses			Contact Hr/Week			Credits			
	C-1-1 Computational method for Communication (DC) (3-1-0) 4	C-1-2 Mobile and Wireless Communication (DC) (3-0-0) 3	C-1-3 Advance Digital Communication (DC) (3-0-0) 3	C-1-4 DE 1 (3-0-0) 3	C-1-5 DE 2 (3-0-0) 3	Foreign Language (1-0-2) 2	Communication System Lab (0-0-4) 2	C-2-1 Detection and Estimation Theory (DC) (3-0-0) 3	C-2-2 Advance Optical Communication (DC) (3-0-0) 3	C-2-3 Advance Digital Signal Processing (3-0-0) 3 DE 3	C-2-4 DE 3 (3-0-0) 3	C-2-5 OE-1 (3-0-0) 3	Technical Communication (1-0-2) 2	Modelling and Simulation Lab (0-0-4) 2	L	T		P	Total	
															6	16		1	6	23
C-1-1																				
C-2-1																				
Seminar (0-0-4) 2																				
Dissertation-II (0-0-32) 16																				
C-X-Y (Course-Semester-Paper No.); L-lecture, T-tutorial, P-practical															0	0	0	28	28	16
															0	0	0	32	32	16
Total Credits - 71																				

[Signature]
16/11/14

[Signature]
16/11/14

F. Mr. ...
16/11/14
[Signature]
16/11/14

[Signature]
16/11/14
[Signature]

Legends:

Language	: 02
Technical Communication	: 02
Departmental Core (DC)	: 19
Practicals	: 04
Departmental Elective (DE)	: 09
Open Elective (OE)	: 03
Seminar	: 02
Project	: 04
Dissertation (Semester III and IV)	: 26
TOTAL:	71

LIST OF DEPARTMENTAL ELECTIVES

1. Information Theory and Coding
2. Antenna
3. Modern Telephone Switching System
4. Satellite Communication
5. Microwave and Radar System
6. Digital Image Processing
7. RF System design for wireless communications
8. Adaptive Signal Processing
9. Remote Sensing
10. Communication Networks
11. Spread Spectrum and CDMA system
12. Wireless Security
13. Fractional FFT
14. Wireless Broadband Networks
15. Multimedia Compression Techniques

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16/06/14
16/06/14
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16/06/14
16/6
16/06/14

LIST OF DEPARTMENTAL ELECTIVES

1. Information Theory and Coding
2. Antenna
3. Modern Telephone Switching System
4. Satellite Communication
5. Microwave and Radar System
6. Digital Image Processing
7. RF System design for wireless communications
8. Adaptive Signal Processing
9. Remote Sensing
10. Communication Networks
11. Spread Spectrum and CDMA system
12. Wireless Security
13. Fractional FFT
14. Wireless Broadband Networks
15. Multimedia Compression Techniques

F.M. Muthu
16/6/14

16/6/14

Chandrasekhar
16/6/14

16/6/14

16/6/14

16/6/14

16/6/14

16/6/14

**Department of Electronics and Communication Engineering
School of Engineering and Technology
Sharda University**

Minutes of meeting of Board of Studies (BOS)

A meeting of Board of Studies (BOS) of the department of Electronics and Communication Engineering (ECE) was held on February 26, 2014 at 11:00 am in ECE Board Room (402), SET-I, Sharda University. It was proposed that there will be two streams of the M.Tech. programmes (i) M.Tech. with course work and dissertation (already existing) and (ii) M.Tech. with course work only

1. M.Tech. in ECE with specialization in Microwave Technology.
2. M.Tech. in ECE with specialization in VLSI Technology.

The list of members of the BOS is enclosed.

The course structure of the two programmes was presented and explained to the members. The experts critically examined and discussed the proposal. The experts suggested few Departmental Electives (DE) which have been included in the list of DE. The BOS approved the course structure of the two streams.

Following members were present.

- | | |
|-----------------------------|---|
| 1. Prof. R. M. Mehra | <i>R.M. Mehra</i>
26.2.2014 |
| 2. Dr. Amitava Majumdar | <i>Amitava Majumdar</i>
26/2/14 |
| 3. Prof. Avinashi Kapoor | <i>Avinashi Kapoor</i>
26.2.14 |
| 4. Dr. Ghanshyam Singh | <i>Ghanshyam Singh</i>
26.2.14 |
| 5. Mr. Sandeep Singh (AP) | <i>Sandeep Singh</i>
26.2.14 |
| 6. Ms. Rashmi Priyadarshini | <i>Rashmi Priyadarshini</i>
26/02/14 |
| 7. Ms. Noor-e-Zahra (AP) | <i>Noor-e-Zahra</i>
26/02/14 |
| 8. Mr. C. Mohan | <i>C. Mohan</i>
26/02/2014 |
| 9. Mr. Zeeshan Haider | <i>Zeeshan Haider</i>
26/02/2014 |

Course Structure
M.Tech. in ECE with specialization in VLSI Technology, Sharda University
(By Course Work)

SEMESTER	Courses (L-T-P) Credits										Contact Hr/Week			Credits
	C-1-1 Advanced Computer Architecture (DC)(3-0-0) 3	C-1-2 Advance Digital design using HDL (DC)(3-1-0)4	C-1-3 Mobile and Wireless Communication (DC) (3-0-0)3	C-1-4 DE I (3-0-0)3	C-1-5 DE II (3-0-0)3	Foreign Language (1-0-2) 2	Modelling Lab (0-0-4)2	Lecture Courses	L	T	P	Total		
I									6	16	1	6	23	20
II	C-2-1 Mixed Signal CMOS VLSI Design (DC) (3-0-0)3	C-2-2 CMOS Digital VLSI Design, Testing and verification (DC) (3-0-0)3	C-2-3 Advanced VLSI Design (DC) (3-0-0)3	C-2-4 DE-III (3-0-0)3	C-2-5 OE-I (3-0-0) 3	Technical Presentation (1-0-2) 2	Electronics CAD Lab (0-0-4)2	6	16	0	6	22	19	
III	Seminar-I (0-0-4)2	Project-1 (0-0-8)4	DE-IV (3-0-0)3	DE-V (3-0-0)3	OE-II (3-0-0) 3			3	9	0	12	21	15	
IV	Seminar--II (0-0-4)2	Project-2 (0-0-12)6	DE-VI (3-0-0)3	DE-VII (3-0-0)3	OE-III (3-0-0) 3			3	9	0	16	25	17	
												Total Credits - 71		

C-X-Y (Course-Semester-Paper No.); L-lecture, T-tutorial, P-practical

Approved by
26/02/14

November
26/02/14

[Signature]
26/02/14

[Signature]
26/02/2014

[Signature]
26/02/14

[Signature]
26.2.2014

[Signature]
26.2.14

[Signature]
26/02/14

LIST OF DEPARTMENTAL CORE

1. Advanced Computer Architecture
2. Advance Digital design using HDL
3. CMOS Digital VLSI Design, Testing and verification
4. Mobile and Wireless Communication
5. Advanced VLSI Design
6. VLSI Signal Processing
7. Device Modeling and Circuit Simulation
8. Analog IC design

LIST OF DEPARTMENTAL ELECTIVES

1. Nanoelectronics
2. Biomedical Electronics
3. Yield and Reliability Engineering
4. Analog IC Design
5. MEMS and IC Integration
6. Advanced Digital Communication System
7. ASIC/FPGA Design
8. Device Modeling and Circuit simulation
9. Advanced DSP
10. Embedded Systems
11. MICROWIND DSCH and its applications
12. ATLAS and its applications

Dr. M. S. ...
[Signature]
26/12/2017

[Signature]
26/2/17

[Signature]
26/12/17
HILEG

[Signature]
26/12/17

[Signature]
26/12/17

[Signature]
26/12/17

Course Structure
M.Tech. in ECE with specialization in VLSI Technology, Sharda University
(By Course Work and Dissertation)

SEMESTER	Lecture Course										Contact Hr/Week			Credits
	C-1-1	C-1-2	C-1-3	C-1-4	C-1-5	Foreign Language	Practical Modelling Lab	L	T	P	Total			
I	C-1-1 Advanced Computer Architecture (DC)(3-0-0) 3	C-1-2 Advance Digital design using HDL (DC)(3-1-0)4	C-1-3 Advanced VLSI Design (DC) (3-0-0) 3	C-1-4 DE I (3-0-0)3	C-1-5 DE II (3-0-0)3	Foreign Language (1-0-2) 2	Practical Modelling Lab (0-0-4)2	16	1	6	23	20		
II	C-2-1 Mixed Signal CMOS VLSI Design (DC) (3-0-0)0	C-2-2 CMOS Digital VLSI Design, Testing and verification (DC) (3-0-0)3	C-2-3 Mobile and Wireless Communication (DC) (3-0-0)3	C-2-4 DE-III (3-0-0)3	C-2-5 OE (3-0-0) 3	Technical Presentation (1-0-2) 2	Practical Electronics CAD Lab (0-0-4)2	16	0	6	22	19		
III	Seminar (0-0-4)2	Project (0-0-8)4	Dissertation-I (0-0-20)10					0	0	32	32	16		
IV	Dissertation-II (0-0-32)16							0	0	32	32	16		
Total Credits – 71														

C-X-Y (Course-Semester-Paper No.); L-lecture, T-tutorial, P-practical

Minutes of meeting of BOS of ECE held on June 11, 2015

A meeting of the Board of studies was held on 11th June 2015 at Block-I 402, ECE Boardroom at 2:00 pm.

The following members were present.

1. Prof. Raj Senani, NSIT
2. Prof. Avinashi Kapoor, University of Delhi
3. Dr. Amitava Majumdar, Moser Baer India
4. Prof. R. M. Mehra
5. Prof. Abdhesh Singh (HoD), ECE
6. Dr. Pallavi Gupta
7. Mr. Sandeep Singh
8. Ms Shaheen Naz

The course structure & syllabi of the following courses were considered.

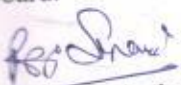
B.Tech. In Electronics and Communication

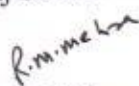
M.Tech.in ECE with Specialization:

1. Microwave Technology
2. VLSI Technology
3. Digital Communication (NEW)
4. Electronic System Design and Management (NEW)
5. Optical Communication (NEW)
6. Digital Signal Processing (NEW)


All the course structures and syllabi were discussed and approved with minor modification in the syllabus of the subject EMFT. One unit of transmission lines be included. The change has been incorporated.

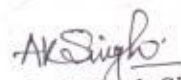
It is recommended that the details of the above approved courses be placed before the faculty Board.

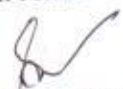

Prof. Raj Senani



Prof. R. M. Mehra

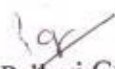

Mr. Sandeep Singh


Prof. Avinashi Kapoor


Prof. Abdhesh Singh


Ms Shaheen Naz


Dr. Amitava Majumdar


Dr. Pallavi Gupta

Approval for course structure

2 messages

Dr. Abdhesh Kumar Singh <abdhesh.singh@sharda.ac.in>

Wed, May 31, 2017 at 2:13 PM

To: Raj Senani <dr_senani@yahoo.com>, avinashi_kapoor@yahoo.com

Dear Sir

Pl find attached herewith the course structure of 2016-20 and 2017-21 along with modifications/changes. The documents are as follows:

- Course Structure 2016-2020 (No change in 1st Year)
- Course Structure 2017-2021.
- Modification/Changes.

This needs your kind approval as a BOS member.

—

Thanks & Regards

Dr. A.K. Singh

Head of Department

Electronics & Communication Department

(91+9891410081)

Go Green, save Water,

3 attachments



ECE.course Structure.2016-20.doc

85K



ECE.course Structure.2017-21.doc

90K



Modification in course structure.docx

18K

Dr. Raj Senani <dr_senani@yahoo.com>

Wed, May 31, 2017 at 2:47 PM

Reply-To: "Dr. Raj Senani" <dr_senani@yahoo.com>

To: "Dr. Abdhesh Kumar Singh" <abdhesh.singh@sharda.ac.in>

Dear Prof Singh

The attached course structure is hereby Approved.

sincerely,

Dr. Raj Senani

Member Sigma Xi, Senior Member IEEE, FIE, FIETE, F.N.A.Sc.
(former Director, NSIT: 2008-2014)
Professor,
Division of Electronics and Communication Engineering
Netaji Subhas Institute of Technology
Sector 3, Dwarka
New Delhi 110078

e-mail: senani@ieee.org

Mobile: +91-9899168678

Official web-page: <http://www.nsit.ac.in/faculty/rs/>

Personal home page: www.angelfire.com/in/RajSenani97

* Also working as Adjunct Professor, National Institute of Technology, Patna, India.

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SHARDA UNIVERSITY
SCHOOL OF ENGINEERING & TECHNOLOGY
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Date: 04/06/2018

A meeting of board of studies of ECE will be held on 5th June 2018, at 01:00 PM in Room No.005, block I

Chairman: Dr. Pallavi Gupta, HOD

LIST OF PROPOSED MEMBERS OF BOARD OF STUDIES (BOS)

- A. From University**
1. **Prof. Avinashi Kapoor**
Department of Electronic Science
University of Delhi South Campus
New Delhi-110 021
E-mail: avinashi_kapoor@yahoo.com
Mobile: 09350571397
 2. **Dr Ajay Agarwal**
CEERI, CSIR,
Pilani
Mob - 8058598903
- B. From Industry**
1. **Mr. Shekhar Singhal**
Module Lead,
Logic Fruit Technologies,
Sector 30, Gurugram, India
Email ID- shekhar.singhal6@gmail.com
Mobile- 08527801799
 2. **Dr. Rajeev Kumar Srivastava**
Director
Design Platforms, NXP Semiconductors India Private Limited
Information Technology Park
Nagawara Village, Kasaba Hobli
Bangalore 560045
E-mail: rajeev.rajeevsrivastava@gmail.com
Mobile: 07259219237
 3. **Mr. Alok Mittal**
Senior Technical Officer
Advanced manufacturing lab
ST Microelectronics, Greater Noida,
E.Mail: alok.mittal@st.com,
Mobile: 9810060432
- C. Internal BOS members**
1. **Prof. R. M. Mehra**
E-mail: rm.mehra@sharda.ac.in



SHARDA UNIVERSITY
SCHOOL OF ENGINEERING & TECHNOLOGY
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Date: 04/06/2018

A meeting of board of studies of ECE will be held on 5th June 2018, at 01:00 PM in Room No.005, block I

Chairman: Dr. Pallavi Gupta, HOD

LIST OF PROPOSED MEMBERS OF BOARD OF STUDIES (BOS)

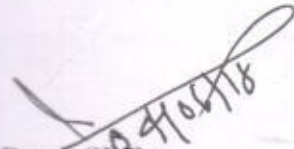
- A. From University**
1. **Prof. Avinashi Kapoor**
Department of Electronic Science
University of Delhi South Campus
New Delhi-110 021
E-mail: avinashi_kapoor@yahoo.com
Mobile: 09350571397
 2. **Dr Ajay Agarwal**
CEERI, CSIR,
Pilani
Mob - 8058598903
- B. From Industry**
1. **Mr. Shekhar Singhal**
Module Lead,
Logic Fruit Technologies,
Sector 30, Gurugram, India
Email ID- shekhar.singhal6@gmail.com
Mobile- 08527801799
 2. **Dr. Rajeev Kumar Srivastava**
Director
Design Platforms, NXP Semiconductors India Private Limited
Information Technology Park
Nagawara Village, Kasaba Hobli
Bangalore 560045
E-mail: rajeev.rajeevsrivastava@gmail.com
Mobile: 07259219237
 3. **Mr. Alok Mittal**
Senior Technical Officer
Advanced manufacturing lab
ST Microelectronics, Greater Noida,
E.Mail: alok.mittal@st.com,
Mobile: 9810060432
- C. Internal BOS members**
1. **Prof. R. M. Mehra**
E-mail: rm.mehra@sharda.ac.in

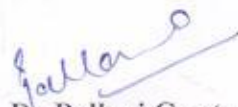
- Mobile: 9971271374
2. **Dr. Rashmi Priyadarshini**
E-mail: rashmi.priyadarshini@sharda.ac.in
Mobile: 9911450973
 3. **Mr. Sandeep Singh**
E-mail: sandeepsingh.ec@sharda.ac.in
Mobile: 9891833637
 4. **Mrs. Shaheen Naaz**
Email: Shaheen.naaz@sharda.ac.in
Mobile: 989899454758
 5. **Mr. Shiv Pujan Jaiswal**
EMail: shivapujan.jaiswal@sharda.ac.in
Mobile: 8826998122
 6. **Mr. C. Mohan**
EMail: c.mohan@sharda.ac.in
Mobile: 9811090352

Following are the agenda of the meeting :

1. Approval of new Course Structure of B.Tech., Electronics & Communication Engineering for 2018 Batch. (160 Credit)
2. Approval of new program of B.Sc (Hons) Electronics with specialization in Robotics and Application.
3. Approval of Syllabi of Above programs.
4. Approval of few syllabi of B.Tech. Electrical & Electronics Engineering, B.Tech. CSE with specialization in IoT.
5. Approval of few syllabi of M. Tech. Programmes.
6. Any other item.

All members are requested to make it convenient to attend the meeting.


Dean, SET


Dr. Pallavi Gupta

HOD, ECE & EE Deptt.

Minutes of Meeting of BOS of ECE - M.Tech VLSI

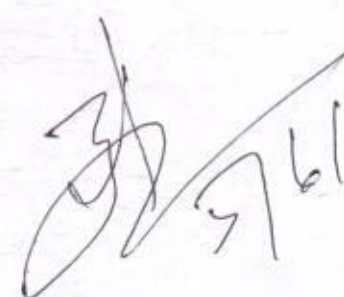
The following changes in Course Structure & syllabi of the following M.Tech (VLSI Technology).

For Batch 2018-2020

1. VHDL may be replaced by Verilog /System Verilog.
2. A transcript language (system c / Tcl / perl) may be added along with its lab.
3. Back End Design Lab may be added.
4. Practical like CPU designing and protocol (I2C/SPI/UART/ETHERNET) may be added to the existing labs.


05/06/18


5/6/18


5/6/18

Department of Electronic and Communication Engineering
School of Engineering and Technology
Sharda University
Minutes of Meeting of External BoS(session 2018 onwards)

Following members were present in the External BoS for various programs on 05/06/2018 at 005, board room, block-I Chaired by Dr. Pallavi Gupta, HoD, ECE&EEE

A. From other University

1. Prof. Avinashi Kapoor
Department of Electronic Science
University of Delhi South Campus
New Delhi-110 021
E-mail: avinashi_kapoor@yahoo.com
Mobile: 09350571397

2: Dr Ajay Agarwal

CEERI, CSIR,
Pilani
Mob o8058598903

B. From Industry

1. **Mr. Shekhar Singhal**
Module Lead,
Logic Fruit Technologies,
Sector 30, Gurugram, India
Email ID- shekhar.singhal6@gmail.com
Mobile- 08527801799

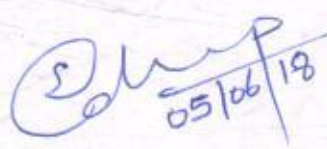
2. Dr. Rajeev Kumar Srivastava
Director
Design Platforms, NXP Semiconductors India Private Limited
Information Technology Park
Nagawara Village, Kasaba Hobli
Bangalore 560045
E-mail: rajeev.rajeevsrivastava@gmail.com
Mobile: 07259219237

3. Mr.Alok Mittal
Senior Technical Officer
Advanced manufacturing lab

ST Microelectronics
Greater Noida
E.Mail:Alok.mittal@st.com
Mobile:9810060432

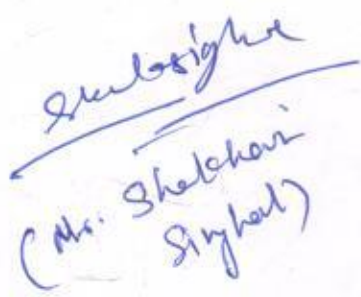
C. Internal members

1. Dr. Pallavi Gupta(HOD)
(Associate Professor)
2. Prof. R. M. Mehra
3. Dr. Rashmi Priyadarshini
4. Mr. Sandeep Singh
5. Mrs. Shaheen Naaz
6. Mr. Shiv Pujan Jaiswal
7. Mr. C Mohan
8. Ms Manisha Rajoriya
9. Ms. Ritu Singh
10. Mr. Prince Nagar


05/06/18

Manisha Rajoriya




(Mr. Shalochan Singh)


5/6/18
(Prof. Animesh Kapur)







Minutes B.Tech Course Structure - ECE

1. In Introduction to Engineers syllabus to be modified including Blue Brain, 5G, in place of AI.
2. Include subject as Smart Materials as elective.
3. Summer Internship in 1st year is to be reviewed.
4. Soft skill may be changed to audit course, to save our credits for core subjects like LIC.
5. Introduction to biology may be changed to Biomedical Engineering.
6. In DSP Book, include book DSP BY Rabiner and Gold, Pearson publication.
7. In tinkering lab syllabus, update syllabus of unit 3 and 4.
8. In Lab we should include protocol analyzer (open source).
9. PCB Design tools may be incorporated in syllabus of Tinkering lab.
10. Security and power saving techniques may be included in syllabus of IOT and Basics of drone technology
11. In 6th semester, if students are taking Embedded system as an elective, the project based learning will be lab for Embedded system based on STM32.
12. There should be one additional subject may be included in the program including differential amplifier, level shifter and current mirror and CMOS Technology.

Sen
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initial

Mulligal

Edup
05/06/18

Pallavi

for

[Signature]

Course Structure

M.Tech. in ECE with specialization in VLSI Technology, Sharda University







SEMESTER	Courses (L-T-P)						Lecture	
	C-1-1	C-1-2	C-1-3	C-1-4 and DE 1	C-1-5 OE	C-1-6 Technical Presentation		Practical
I	C-1-1 Advanced Computer Architecture (DC) (3-0-0) 3	C-1-2 Advance Digital design using HDL (DC) (3-1-0) 4	C-1-3 Mobile Wireless Communicati on (DC) (3-0-0) 3	C-1-4 DE 1 (3-0-0) 3	C-1-5 OE (3-0-0) 3	C-1-6 Technical Presentation (1-0-2) 2	Practical Digital System Presentation Laboratory (or) Modelling Lab (0-0-4)2	6
II	C-2-1 CMOS Digital VLSI Design, Testing and verification (DC) (3-0-0) 3	C-2-2 Advanced VLSI Design (DC) (3-0-0) 3	C-2-3 Mixed CMOS Design (DC) (3-0-0)3	C-2-4 DE 3 (3-0-0) 3	C-2-5 DE 2 (3-0-0) 3	C-2-6 Term paper (0-2-0) 2	Practical Physical Design Lab (or)Electronics CAD Lab (0-0-4)2	6
III	Seminar (0-0-4)2	Project (0-0-8)4	Dissertation (0-0-20) 10					-
IV	Dissertation (0-0-32)16							-

C-X-Y (Course-Semester-Paper No.); L-lecture, T-tutorial, P-practical

FD 11/10/15
BOS recommends
Approved

SUMMARY SHEET

Teaching Department Electronics and Communication Engineering
School School of Engineering and Technology
Name of Course M.Tech. In Electronics and Communication
Specialization in-
Microwave Technology
VLSI Technology
Digital Communication
Electronic System Designing and Management
Optical Communication
Digital Signal Processing
Duration Two (02) years
Total No. of Credits 71
Date of meetings of BOS June, 11, 2015
Member of BOS

1. Prof. Abdhesh Singh (HoD), ECE 
2. Prof. R. M. Mehra
3. Dr. Pallavi Gupta 
4. Mr. Sandeep Singh 
5. Ms Shaheen Naz 
6. Ms. Rashmi Priyadarshini 
7. Mr. Yogendra Singh Gill 


11/6/15
Chopra...

Department of Electronic and Communication Engineering
School of Engineering and Technology
Sharda University

LIST OF PROPOSED MEMBERS OF BOARD OF STUDIES (BOS)

A. From University/IIT

✓ 1. Prof. Raj Senani
Division of Electronics and Communication Engineering
Netaji Subhas Institute of Technology
Sector-3, Dwarka, New Delhi
Dr_senani@yahoo.com
Mobile 9899168678

✓ 2. Prof. Avinashi Kapoor
Department of Electronic Science
University of Delhi South Campus
New Delhi-110 021
E-mail: avinashi_kapoor@yahoo.com
Mobile: 09350571397

B. From Industry

✓ 3. Dr. Amitava Majumdar
General Manager - Engineering and R&D
Moser Baer India Limited
66 Udyog Vihar, Greater Noida
Uttar Pradesh-201306
E-mail: a.majumdar@moserbaer.in
Mobile: 09810834211

C. From Department of Electronic and Communication Engineering
School of Engineering and Technology
Sharda University

4. Prof. Abdhesh Singh (HoD)
abdhesh.singh@sharda.ac.in
Mobile: 9891410081

5. Prof. R. M. Mehra
E-mail: rm.mehra@sharda.ac.in
Mobile: 9971271374

6. Dr. Pallavi Gupta (Associate Professor)
pallavi.gupta2@sharda.ac.in
Mobile: 8800998813