

SUMMARY SHEET

Department: Electronics and Communication Engineering
School: School of Engineering and Technology
Name of the Course: Ongoing B.Tech. in ECE (Term-VII and Term-VIII)
Total No. of Credits: 58
Date of meeting of BOS: July 12, 2012
Status: Approved by BOS

Members of BOS:	1. Prof. R. M. Mehra, HOD, ECE	Chairman
	2. Prof. H. M. Gupta, IIT Delhi	External Member
	3. Prof. Avinashi Kapoor, University of Delhi	External Member
	4. Dr. Rajeev Srivastava, ST Microelectronics	External Member
	5. Dr. Amitava Majumdar, Moser Baer India	External Member
	6. Mr. Sandeep Singh, Coordinator, ECE	Internal member
	7. Ms Rashmi Priyadarshini, ECE	Internal member
	8. Ms Noor e Zahra, ECE	Internal member

Date of Meeting of Faculty Board: July 18, 2012
Status: Approved by BOS and Faculty Board

(Signature)
Amitava
Noor

Forwarded message -----

From: **Dr. R. M. Mehra** <rm.mehra@sharda.ac.in>

Date: Fri, 22 Jun 2012 at 18:23

Subject: B.Tech. ECE

: <himgupta@ee.iitd.ac.in>

Prof. H. M. Gupta
Department of Electric Engineering
Delhi

Dear Professor Gupta,

Thank you very much for sparing your valuable time for the combined BOS at Sharda University. Attached please find the modified course structure of ECE as per your advice. Kindly examine approve.

With regards

Anupam
Noss



1.Tech. Electronics and Communication Engineering

Electronics and Communication Courses (L-T-P)

sem.	Electronics and Communication Courses (L-T-P)										Credits					
	Physics (3-1-2) 5 or Chemistry (3-0-2) 4	Principles of Electrical & Electronics Eng. (3-1-2) 5 or Basics of Computers & 'C' Programming (3-1-2) 5	Mathematics-1 (3-1-0) 4 *Mathematics BT-1 *(3-1-0) 4	Geo-Sciences (2-0-0) 2 Or Bio-Sciences (2-0-0) 2	Manufacturing Practices (0-0-4) 2 or Graphic Science (1-0-4) 3	Foreign Language (2-0-2) 3 Or Env. Sc. (2-0-0) 2	Course s	Contact Hours			Total					
L	T	P	L	T	P	L	T	P								
	Chemistry (3-0-2) 4 or Physics (3-1-2) 5	Basics of Computers & 'C' Programming (3-1-2) 5 or Principles of Electrical & Electronics Eng. (3-1-2) 5	Mathematics - 2 (3-1-0) 4 *Mathematics BT-2 *(3-1-0) 4	Bio-Sciences (2-0-0) 2 Or Geo-Sciences (2-0-0) 2	Graphic Science (1-0-4) 3 or Manufacturing Practices (0-0-4) 2	Env. Sc. (2-0-0) 2 Or Foreign Language (2-0-2) 3	6	14/13	2/3	8/1	24/26	21/20				
	Analog Circuits (DC) (3-1-2) 5 OE I (3-1-0) 4	Digital Electronics I (DC) (3-1-2) 5 Digital Electronics II (DC) (3-1-2) 5	Engg Maths (BS) (3-1-0) 4 EMFT (DC) (3-1-0) 4	PCM (HM) (2-0-2) 3 HMM (HM) (3-0-0) 3	Signal System (DC) (3-1-2) 5 Microprocessor and Microcontrollers and application (DC) (3-1-2) 5 Microwave Engg. (DC) (3-1-2) 5		5	14	4	8	26	22				
	Linear Integrated Circuit (DC) (3-1-2) 5 Antenne and Radar Engg. (DC) (3-1-0) 4	Communication Engg. (DC) (3-1-2) 5 Digital Communication (DC) (3-1-2) 5	DE I (3-1-2) 5 DSP (DC) (3-1-2) 5	OE I (3-1-0) 4 DE I (3-1-2) 5	OE I (3-1-0) 4		5	15	4	4	23	21				
	VLSI (DC) (3-1-2) 5 DE III (3-1-2) 5	DE II (3-1-2) 5 OE II (3-1-0) 4	DE II (3-1-0) 4 DE II (3-1-0) 4	Phonic Devices (DC) (3-0-0) 3 Project 8	PCM (HM) (3-0-2) 4 HMM (HM) (3-0-0) 3	Project 2	0	0	0	0	0	2				
	Summer Internship 2															
	TOTAL										40	11	2	5	194	180

Handwritten signature and notes:
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Legends:

- BS:** Basic Sciences
- DC:** Departmental Core
- DE:** Departmental Elective
- OE:** Open Elective
- HM:** Humanities

List of Courses

Departmental Core

- Analog Circuits
- Digital Electronics I & II
- Signal system
- EMFT (Electromagnetic Field Theory)
- Linear Integrated Circuits
- Communication Engineering
- Microwave Engineering
- Antenna and Radar Engineering
- Digital Communication
- Digital Signal Processing
- Microprocessor and Microcontrollers and application
- VLSI
- Photonics Devices

Departmental Elective I

- Embedded Systems
- Control system
- Information Theory and Coding

Departmental Elective II

- Wireless and mobile communication
- Satellite Communication
- Optical Communication
- Energy Generation and Conversion

Departmental Elective III

- Advance VLSI
- Digital logic design
- Advance DSP
- Data Communication network
- Telecom Switching
- Image Processing
- Speech Processing

Open Elective I

- Network Analysis and Synthesis
- Power devices and Circuits
- Industrial Instrumentation
- Material Sciences

Open Elective II

- Programming in JAVA
- Mobile Computing
- MEMS and System on Chip
- Biomedical Instrumentation
- Robotics and Mechatronics

Chandran

Edup

Noor

Arshad



Rashmi Priyadarshini (Suset Associate Professor) <rashmi.priyadarshini@sharda.ac

Fwd: REQUEST/BOS/Meeting

2 messages

R.M. Mehra (Suset Professor Emeritus) <rm.mehra@sharda.ac.in>

To: Rashmi Priyadarshini <rashmi.priyadarshini@sharda.ac.in>

Tue, Dec 4, 2018 at 1:39

R. M. Mehra
9971271374

----- Forwarded message -----

From: **Dr. R. M. Mehra** <rm.mehra@sharda.ac.in>

Date: Mon, 2 Jul 2012 at 17:07

Subject: REQUEST/BOS/Meeting

To: <jp_sub@yahoo.com>, <jpsrm@mech.iitd.ac.in>

Cc: Ramesh Chander Sachdeva <rc.sachdeva@sharda.ac.in>, Dr. Gopal P. Sinha <gopal.sinha@sharda.ac.in>

To,

Prof. J.P. Subrahmanyam**Head, Dept. of Mechanical Engineering****IIT, Delhi****New Delhi**

E-mail: jp_sub@yahoo.com, jpsrm@mech.iitd.ac.in

Subject: - Invitation for the combined meeting of Board of Studies of School of
Engineering & Technology.

Dear Prof. J.P. Subrahmanyam

It is our privilege to invite you to attend the meeting of Combined Board of Studies (BOS) for the School of Engineering & Technology, Sharda University on 12th July 2012 at 10 am in the T&P Board Room (Ground Floor, PGDM Building). The whole day meeting will be in two sessions with lunch break. The principal agenda will be

1. Course structure and syllabi of M.Tech. programme.
2. Approval of the syllabi of 7th & 8th terms of B.Tech. programme already running.

We look forward to your kind presence at 10:00 am on 12th July 2012 at T&P conference room. A line in confirmation will be highly appreciated.

Department of Electronics and Communication Engineering
School of Engineering and Technology
Sharda University

Minutes of the meeting of Board of Studies (BOS)

A meeting of the Board of Studies (BOS) for the department of electronics and communication engineering was held on June 18, 2013 at 10:00 am in Room No. 220, SET, Sharda University. The syllabi of the following program were discussed:

1. B.Tech. in ECE (182 Credits)
2. B.Tech. in ECE with specialization in Instrumentation and Control (182 Credits)

Following members participated in the meeting:

1. Prof. R. M. Mehra, HoD, ECE	Chairman
2. Prof. Avinashi Kapoor, DU	External Expert
3. Dr. Amitava Majumdar, Moser Baer India	External Expert
4. Mr. Ghanshyam Singh, ECE	Internal member
5. Ms Rashmi Priyadarshini, ECE	Internal member
6. Mr. C. Mohan, EIE	Internal member
7. Mr. Zeeshan Haider, EIE	Internal member

The course structure of the two programmes was presented and explained to the members. The experts critically examined and discussed the course content of each subject and suggested some of the modifications which were recorded. These modifications are enclosed. These modifications have been incorporated in the syllabi of the programmes.

The meeting ended with a Vote of Thanks to all the members.


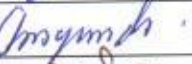


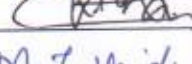


1. Prof. R. M. Mehra *R.M.Mehra 18.6.2013*
2. Prof. Avinashi Kapoor *[Signature] 18.6.13*
3. Dr. Amitava Majumdar *[Signature] 18/6/13*
4. Mr. Ghanshyam Singh *[Signature] 18.6.13*
5. Ms Rashmi Priyadarshini *[Signature]*
6. Mr. C. Mohan *[Signature]*
7. Mr. Zeeshan Haider *M.Z. Haider*

Department of Electronic and Communication Engineering
School of Engineering and Technology
Sharda University

Board of Studies

Date: 18/06/2013

Attendance Sheet

S. No.	Name		Signature
1.	Prof. R. M. Mehra, HoD, ECE	Chairman	
2.	Prof. Avinashi Kapoor, DU	External Expert	
3.	Dr. Amitava Majumdar, Moser Baer India	External Expert	
4.	Mr. Ghanshyam Singh, ECE	Internal member	
5.	Ms Rashmi Priyadarshini, ECE	Internal member	
6.	Mr. C. Mohan, EIE	Internal member	
7.	Mr. Zeeshan Haider, EIE	Internal member	

Minutes of Meeting of BOS

BOS meeting for ECE Dept. was held on 18 Jan 2013 in Room No. 220, SET 1. The following members were present in the meeting:

1. Dr. Avinashi Kapoor(DSW, DU) External BOS member.
2. Dr. Amitabh Majoomdar (Moserbaer, Senior E.O.) External BOS member.
3. Prof. R.M. Mehra (HOD, ECE Deptt.) Internal BOS member
4. Dr. Ghanshyam Singh (Associate Prof. ECE Dept.) Internal BOS member
5. Mrs. Rashmi Priyadarshini (Asst. Prof. ECE Dept.) Internal BOS member
6. Other faculty members of ECE Dept.

The following modifications are done in the meeting.

1. Data structure should be proposed as open elective.
2. Concepts of 'C' should be included.
3. Project should be industry based project.
4. The name of photonic devices(ECE407) should be changed to Photovoltaic Devices
5. Printed electronics course is suggested by Dr. A. Majumdar.
6. Neuro Fuzzy and generic algorithms should be included in Control Systems
7. Sequential machine should be included in digital courses.
8. ECE303 (DSP) Suggestion in first order design of FIR, IIR & DFT & FFT.
9. In digital logic design asynchronous machines should be highlighted
10. ECE206 (DE-II) In DE-II Unit B topic 2 sequential machine should be introduced.
11. ECE205(S&S) In SS course objective: Emphasis should be on problem solving techniques.
12. ECE301 (LIC) LIC UNIT B (topic 2) slew rate should be introduced.
13. EIE301Control system: course objective: will develop skill to control various parameters like temperature, flow rate etc. using genetic algorithms based PID.
14. ECE306 Digital Logic Design: Introduction of algorithmic state machine should be included.
15. Microwave: change name: Introduction to Microwave Engineering.
16. Energy Conversion ECE 404: change verbs of course outcomes
17. ECE 404 (Optical Communication) : Course Objective: Make student understand difference between waveguide and optical fibres and their merits & demerits.

18. ECE-407 (change course name: Photovoltaic devices and objective include defining various parameters that characterizes a solar cell. UNIT-A (topic 2): replace conversion efficiency by parameters of solar cell.
19. ECE 013: Course name to be changed from Electronic Switching to Telecommunication Switching.
20. ECE 017 (analog & Digital Communication): replace all objective to single objective as : to gain knowledge of various types of techniques in analog & digital communication.
21. ECE 018 : Analog & Digital Electronic replaces as Analog & Digital Electronics and Merge first two objectives as It will student understand various semiconductor devices such as Diode, BJT, FET, MOSFET and their functioning & biasing.
22. ECE 016 (Advanced Semiconductor Devices): replace describe by introduce in objective one.



Dr. Avinashi Kapoor

(DSW, DU)

R.M. Mehta
18.6.2013



Dr. Amitabh Majoomdar

(Senior E.O., Moserbaer)

Minutes of meeting of BOS of ECE held on June 11, 2015

A meeting of the Board of studies was held on 11th June 2015 at Block-I 402, ECE Boardroom at 2:00 pm.

The following members were present.

1. Prof. Raj Senani, NSIT
2. Prof. Avinashi Kapoor, University of Delhi
3. Dr. Amitava Majumdar, Moser Baer India
4. Prof. R. M. Mehra
5. Prof. Abdhesh Singh (HoD), ECE
6. Dr. Pallavi Gupta
7. Mr. Sandeep Singh
8. Ms Shaheen Naz

The course structure & syllabi of the following courses were considered.

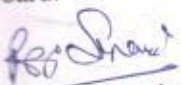
B.Tech. In Electronics and Communication

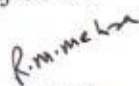
M.Tech.in ECE with Specialization:

1. Microwave Technology
2. VLSI Technology
3. Digital Communication (NEW)
4. Electronic System Design and Management (NEW)
5. Optical Communication (NEW)
6. Digital Signal Processing (NEW)


All the course structures and syllabi were discussed and approved with minor modification in the syllabus of the subject EMFT. One unit of transmission lines be included. The change has been incorporated.

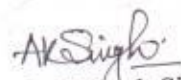
It is recommended that the details of the above approved courses be placed before the faculty Board.

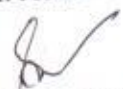

Prof. Raj Senani



Prof. R. M. Mehra

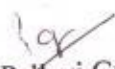

Mr. Sandeep Singh


Prof. Avinashi Kapoor


Prof. Abdhesh Singh


Ms Shaheen Naz


Dr. Amitava Majumdar


Dr. Pallavi Gupta

B.Tech.

in

Electronics and Communication Engineering
COURSE STRUCTURE & SYLLABI

(2nd to 4th Year With effect from academic session 2015-16)



Department of Electronics and Communication Engineering
School of Engineering and Technology
SHARDA UNIVERSITY

B.Tech First Year Course Structure for 2015

Sem.	COURSE STRUCTURE						Courses	L	T	P	Weekly Contact	Credit
	Physics-2 PHY-112 (3-1-2)5	Logic Building in problem solving CSE-105 (3-1-2)5	Mathematics-1 MTH-112 (3-2-0)4	Env.Sc. EVS-103 (2-0-0)2	Manufacturing Science EEE 154 (0-0-4)2	Functional English-I ENG-102 (2-0-2)3						
I							6	13	4	10	27	21
II	Chemistry CHY-105 (3-1-2)5	Programming using 'C' CSE-106 (3-0-2)4	Mathematics-2 MTH-113 (3-2-0)4	Physics-4 PHY-114 (3-1-0)4	PEEE EEE-110 (3-1-2)5	Functional English-II ENG-103 (2-0-2)3	6	17	5	8	30	25
											Total	46

B. Tech ECE- Session 2012-2016

Sem.	Courses (L-T-P)										Courses	Contact Hours				Credits
	III	IV	V	VI	Summer Internship (ECE334)					VII		VII I	L	T	P	
III	Analog Circuits (ECE231)(DC) (3-1-2) 5	Digital Electronics- (ECE232)(DC) (3-1-2) 5	Mathematics-3 (Engg Maths) (MTH217) (3-1-0) 4	Managerial Economics (HMM206) (3-0-0) 3	Signals and Systems (ECE233) (DC) (3-1-0) 4	Term Paper (ECE281) (0-0-2) 1	6	15	4	6	24	22				
IV	Digital Electronics- II (DC) (ECE234) (3-1-0) 4	Network Analysis and Synthesis (DC) (ECE237) (3-1-2) 5	EMFT (DC) (ECE235) (3-1-0) 4	Communication Practices (ENG201) (2-0-2) 3	Microprocessors and interfacing (ECE236)(DC) (3-1-2) 5	Term Paper (ECE282) (0-0-2) 1	7	17	4	10	30	26				
V	Linear Integrated Circuit (ECE331) (DC) (3-1-2) 5	Communication Engg. (ECE332) (DC) (3-1-0) 4	DE I (3-1-2) 5	OE I (4-0-0) 4	DSP (DC) (ECE333) (3-1-2) 5	Term Paper (ECE283) (0-0-2) 1	7	19	4	8	30	27				
VI	Digital Communication (DC) (ECE334) (3-1-2) 5	DEII (3-1-2) 5	Microcontroller s and Applications (DC) (ECE335) (3-1-2) 5	OE II (4-0-0) 4	Professional Skills Enhancement t(ENG301) (2-0-2) 3	Term Paper (ECE284) (0-0-2) 1	6	15	3	10	27	23				
VII	CMOS VLSI Design (DC) (ECE431) (3-1-2) 5	DE III (3-0-0) 3	DE IV (3-0-2) 4	OE III (4-0-0) 4	Human Resource Management (HMM405) (3-0-0) 3	Project 2 (ECE491)	0	0	0	0	0	02				
VII I	DE V (3-0-0)3	OE IV (4-0-0) 4	DE VI (3-0-0) 3	Project 8 (ECE492)	Writing For Technical For Purposes (ENG401)(3-0-0)3		5	13	0	0	13	21				
TOTAL												12+ 37= 49	27+95 =122	3+ 16= 19	18+3 8=56 96	51+ 145=1 183

B. Tech Course Structure for 2015-16

Sem.	Courses (L-T-P)						Courses	Contact Hours				Credits	
	Analog Circuits (DC) (3-1-2) 5 NEW <i>ECE-221</i>	Digital Electronics (DC) (3-1-2) 5 <i>ECC-222</i>	Networks and Systems (3-1-2) 5 <i>ECE-223</i>	Mathematics-3 Engg Maths MTH217 /Introductory Mathematics* MTH216 (3-1-0) 4	Communication Practices ENG202 (2-0-2) 3	Term Paper ECP280 (0-0-2) 1		L	T	P	Total		
III							5					28	23
IV	Analog and Digital Circuits (DC) (3-1-2) 5 NEW	Microprocessor and Interfacing ECE236 (DC) (3-0-2) 4	EMFT (DC) ECE235 (3-1-0) 4	Concepts of OOPS programming using Java CMP202 (3-1-2) 5	Economics for Engineers HMM206 (3-0-0) 3	Term Paper ECP281 (0-0-2) 1	5	15	3	8	26	22	
V	Linear Integrated Circuit (DC) ECE331 (3-1-2) 5	Communication Engg. (DC) ECE332 (3-1-2) 5	Digital Signal Processing (DC) ECE333 (3-1-2) 5	Microwave & Radar Engineering (DC) (3-0-2) 5 NEW	Professional Skills Enhancement (2-0-2) 4 ENG301	Term Paper ECP380 (0-0-2) 1	5	15	3	12	30	24	
VI	Digital Communication (DC) ECE334 (3-1-2) 5	Microcontrollers and Applications (DC) ECE 335 (3-0-2) 4	Control System (3-1-2) 5 (DC) ECE341 NEW	Management for Engineers HMM303 (3-0-0) 3	DEI/Math 3* (3-1-0) 4	Term Paper ECP381 (0-0-2) 1	5	15	3	8	26	22	
Summer Internship ECE394 (0-0-4) 2													
VII	CMOS VLSI Design (DC) ECE431 (3-1-2) 5	Advanced Written Communication ENGXXX (3-0-0) 3	Project (0-0-4) 2 ECE490	OE I (3-0-0) 3	DE II (3-1-0) 4	DE III (3-1-2) 5	6	15	3	8	26	22	
VIII	DE IV (3-1-0) 4	OE II (3-0-0) 3	DE V (3-1-0) 4	Project ECE491 (0-0-16) 8	Project Management HMNXXX (3-0-0) 3		5	12	2	16	30	22	
TOTAL							12+ 41= 51	27+ 86= 113	3+1 8= 21	18+ 62= 80	51+ 154= 205	46+ 137= 183	

*For Diploma Holders only

11/6/15
Bas recommends - [Signature]
Approved

Some Modifications/Changes are as Follows for B. Tech 2015

Present B. Tech Courses	Modifications/Changes for B.Tech Courses 2015
Signals and Systems	Networks and Systems ECE 237 III Semester
Networks analysis and Synthesis	
Analog Circuits-I	Analog Circuits Syllabus changed in III semester
Digital electronic II	Merged in Analog and Digital Circuits (New Subject) IV Semester
Managerial Economics	Renamed Economics For Engineers, shifted from III to IV Semester
Communications Practices	Shifted from IV to III Semester
Maths-3	Introductory Mathematics for Diploma Holders and maths-3 for all others in III Semester
Concepts of OOPS programming using C++ CMP202 in IV Semester	Renamed Concepts of OOPS programming using JAVA CMP202 in IV Semester
Microwave & radar Engineering is in departmental elective	Microwave & Radar Engineering included in 5 th semester
Professional skills enhancement	Professional skills enhancement shifted to V semester to VI semester
Organizational behavior	Organizational behavior renamed as Management for engineers now in VI semester shifted from V semester
Control system is in DE	Control system included in as core course in VI semester
Mathematics- 3	Mathematics- 3 is a Departmental Elective for diploma holders (DE-1)
Human resource management	Renamed as project management shifted to VIII semester from VII semester
Writing for technical purposes	Renamed as Advanced Written semester and shifted to VII semester from VIII semester
Department electives are six	Reduced to five
Open elective are four	Reduced to two of each three credits

Prof. S. S. S. recommends
11/6/15

Departmental electives

S.No	School	Discipline	Degree	Term	Course title	L	T	P	Credits	Compulsory or Elective
1	SET	ECE	B.Tech.	6	Mixed Signal CMOS VLSI Design	3	1	0	4	Departmental Elective-1
2	SET	ECE	B.Tech.	6	Real Time operating Systems	3	1	0	4	
3	SET	ECE	B.Tech.	6	Antennas & Wave Propagation	3	1	0	4	
4	SET	ECE	B.Tech.	7	VHDL Design	3	1	0	4	
5	SET	ECE	B.Tech.	7	Programming and Customizing the AVR Microcontroller	3	1	0	4	Departmental Elective-2
6	SET	ECE	B.Tech.	7	RF & Microstrip Antennas	3	1	0	4	Departmental Elective-3
7	SET	ECE	B.Tech.	7	Optical Communications	3	1	2	5	
8	SET	ECE	B.Tech.	7	Image Processing	3	1	2	5	
9	SET	ECE	B.Tech.	7	ASIC/FPGA Design	3	1	2	5	
10	SET	ECE	B.Tech.	7	Advanced Communication System	3	1	2	5	
11	SET	ECE	B.Tech.	8	Telecommunication Switching	3	1	0	4	Departmental Elective-4
12	SET	ECE	B.Tech.	8	Satellite Communications	3	1	0	4	
13	SET	ECE	B.Tech.	8	Data Communication Network	3	1	0	4	
14	SET	ECE	B.Tech.	8	Wireless & Mobile Communication	3	1	0	4	
15	SET	ECE	B.Tech.	8	CMOS: Circuit Design, Layout and Simulation	3	1	0	4	Departmental Elective-5
16	SET	ECE	B.Tech.	8	Embedded System	3	1	0	4	
17	SET	ECE	B.Tech.	8	CAD for Patch Antennas Design	3	1	0	4	

Following new subjects has been added in departmental electives

S.No	School	Discipline	Degree	Term	Course code	Course title	L	T	P	Credits	Compulsory or Elective
1	SET	ECE	B.Tech.	5	NEW	CMOS: Circuit Design, Layout and Simulation	3	0	0	3	Departmental Elective-1
2	SET	ECE	B.Tech.	6	NEW	RF and Microstrip patch Antenna	3	0	0	3	Departmental Elective-2
3	SET	ECE	B.Tech.	7	NEW	Real Time operating Systems	3	0	0	3	Departmental Elective-3
4	SET	ECE	B.Tech.	7	NEW	CAD of Patch Antenna for Wireless applications	3	0	2	4	Departmental Elective-4
5	SET	ECE	B.Tech.	8	NEW	Programming and Customizing the AVR Microcontroller	3	0	0	3	Departmental Elective-5
6	SET	ECE	B.Tech.	8	NEW	Mixed Signal CMOS VLSI Design	3	0	0	3	
7	SET	ECE	B.Tech.	8	NEW	Embedded RF System and high frequency Signal Processing	3	0	0	3	

Approved
BOS recommends
11/15

Open electives

S.No	School	Discipline	Degree	Term	Course code	Course title	L	T	P	Credits	Compulsory or Elective
1	SET	ECE	B.Tech.	7	ECE438	Photovoltaic Devices	3	0	0	3	Open Elective-1
2	SET	ECE	B.Tech.	7	ECE471	Consumer Electronics	3	0	0	3	
3	SET	ECE	B.Tech.	7	ECE337	Digital Logic Design	3	0	0	3	
6	SET	ECE	B.Tech.	8	ECE467	Bio-medical Instrumentation	3	0	0	3	Open Elective-2
8	SET	ECE	B.Tech.	8	ECE349	Transducers & Sensor	3	0	0	3	
9	SET	ECE	B.Tech.	8	ECE462	Smart Sensors And Sensors Networking	3	0	0	3	
10	SET	ECE	B.Tech.	8	ECE239	Electrical And Electronic Measurement And Instrumentation	3	0	0	3	

Following new subjects has been added in Open electives

S.No	School	Discipline	Degree	Term	Course code	Course title	L	T	P	Credits	Compulsory or Elective
1	SET	ECE	B.Tech.	7	NEW	Bio-Instrumentation	3	0	0	3	Open Elective-1
2	SET	ECE	B.Tech.	7	NEW	User Interface Design	3	0	0	3	
3	SET	ECE	B.Tech.	8	NEW	Mobile Computing	3	0	0	3	Open Elective-2
4	SET	ECE	B.Tech.	8	NEW	Nano-Electronics	3	0	0	3	

Approved

Recommendations

Prof. S. S. S. S.

BOS

7. Mr. Sandeep Singh (AP)
E-mail: sandeepsingh.ec@sharda.ac.in
Mobile: 9891833637
8. Shaheen Naz
Email: shaheen.naz@sharda.ac.in
Email: 9953980695
9. Ms. Yogendra Singh Gill
E-mail: yogindersingh.gill@sharda.ac.in
Mobile: 0965038533
10. Ms. Rashmi Priyadarshini
E-mail: rashmi.priyadarshini@sharda.ac.in
Mobile: 9911450973

Submitted for consideration and the approval of Dean, SET/ Vice-Chancellor, SU

AK Singh
Dr. A. K. Singh

~~Dean SET~~

May like to approve

AK
10/11/16

~~Vice-Chancellor~~

*Please add me
more person in
Category B.*

Dr. A. K. Singh

SUMMARY SHEET

Teaching Department	Electronics and Communication Engineering
School	School of Engineering and Technology
Name of Course	B.Tech. In Electronics and Communication
Duration	Four (04) years
Total No. of Credits	183
Date of meetings of BOS	June, 11, 2015
Member of BOS	1. Prof. Abdhesh Singh(HoD), ECE 2. Prof. R. M. Mehra 3. Dr. Pallavi Gupta 4. Mr. Sandeep Singh 5. Ms Shaheen Naz 6. Mr. Yogendra Singh Gill 7. Ms. Rashmi Priyadarshini

[Handwritten signatures and dates]
11/6/15
11-6-15
11/6/15

(New)

Minutes of Meeting of BOS of ECE held on May 29, 2017

An internal meeting of Board of Studies was held on 29 May 2017 at 11:00 am, Room No. 302, ECE Board Room .

The following members were present-

- 1-Prof.AK Singh
- 2-Prof.R M Mehra
- 3- Dr. Pallavi Gupta
- 4-Mr.Sandeep Singh
- 5-Dr.Rashmi Priyadarshini
- 6-Ms Shaheen Naz

The Course structure & syllabi of the following B.Tech. batches were discussed:

Batch 2014-18

Batch 2016-20

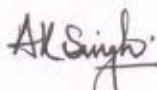
Batch 2017-21

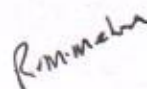
For Batch 2014-18 a new subject Basics of IoT is added in Open Electives list.

The New Course structure and syllabi for Batch 2017-21 were discussed and finalized whereas the modifications in Course Structure and syllabi of Batch 2016-20 were also discussed and finalized.

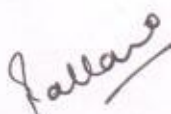
The proposed structures for both the batches the proposed changes and New syllabi Of Basics of IoT(Open Elective;VIII sem),Basics of Electronics Engineering(I Year,All Branches) & Open Source Software(IV sem) are enclosed herewith .

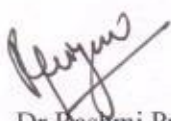
It has been decided that the finalized course structures and modifications be sent to external members of BOS for their consideration and approval.

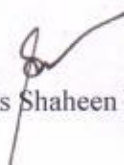

Prof.AK Singh


Prof.R M Mehra


Mr.Sandeep Singh


Dr. Pallavi Gupta


Dr.Rashmi Priyadarshini


Ms Shaheen Naz

Approval for course structure

2 messages

Dr. Abdhesh Kumar Singh <abdhesh.singh@sharda.ac.in>

Wed, May 31, 2017 at 2:13 PM

To: Raj Senani <dr_senani@yahoo.com>, avinashi_kapoor@yahoo.com

Dear Sir

Pl find attached herewith the course structure of 2016-20 and 2017-21 along with modifications/changes. The documents are as follows:

- Course Structure 2016-2020 (No change in 1st Year)
- Course Structure 2017-2021.
- Modification/Changes.

This needs your kind approval as a BOS member.

—

Thanks & Regards

Dr. A.K. Singh


Head of Department


Electronics & Communication Department


(91+9891410081)

Go Green, save Water,

3 attachments

 ECE.course Structure.2016-20.doc
85K

 ECE.course Structure.2017-21.doc
90K

 Modification in course structure.docx
18K

Dr. Raj Senani <dr_senani@yahoo.com>

Wed, May 31, 2017 at 2:47 PM

Reply-To: "Dr. Raj Senani" <dr_senani@yahoo.com>

To: "Dr. Abdhesh Kumar Singh" <abdhesh.singh@sharda.ac.in>

Dear Prof Singh

The attached course structure is hereby Approved.

sincerely,

Dr. Raj Senani

Member Sigma Xi, Senior Member IEEE, FIE, FIETE, F.N.A.Sc.
(former Director, NSIT: 2008-2014)
Professor,
Division of Electronics and Communication Engineering
Netaji Subhas Institute of Technology
Sector 3, Dwarka
New Delhi 110078

e-mail: senani@ieee.org

Mobile: +91-9899168678

Official web-page: <http://www.nsit.ac.in/faculty/rs/>

Personal home page: www.angelfire.com/in/RajSenani97

* Also working as Adjunct Professor, National Institute of Technology, Patna, India.

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Modifications/Changes are as follows for B.Tech. in ECE 2016-20

Present B.Tech Course 2015-16	Modifications/Changes for B.Tech Course 2016-17
First year course structure	First Year course structure remain same as 2015-16
Analog and Digital circuits	Analog and Digital circuits has been separated and contents have been included into Analog Circuits and Digital Electronics.
Mathematics-III has been removed	New course Signals and Systems has been introduced in place of Mathematics-III and
Network and Systems	Network and Systems has been renamed as Network Analysis and Synthesis.
New courses have been added	<ul style="list-style-type: none"> • Introduction to Design (III-Term) • Value and Ethics (III-Term) • Aptitude and Reasoning I – V (From III-Term to VII Term) • English/Humanities-II (III-Term) • Communication Practices-I shifted from III-Term to V- Term • Minor Project-I, II, III. (From IV-Term to VI-Term) & project 1 has been removed • Open source software (IV-Term)
Linear Integrated Circuits in V-Term	Linear Integrated Circuits has been shifted to IV-Term
Project-II in VIII-Term	Credits of Project II in VIII Term is increased from 8 to 10 Credits.
OOP'S with Java	Added to the DE list
Open Elective	Bio Science & GeoScience are included in the list of Openelectives for 7 th Term & 8 th Term

AK Singh


Sharma

Chopra

Modifications/Changes are as follows for B.Tech. in ECE 2017-21

Present B.Tech. Course 2016-17	Modifications/Changes for B.Tech. Course 2017-18
First year course structure	<ul style="list-style-type: none"> • Chemistry has been shifted from II-Term to I-Term. • Logic building and problem solving is replaced by Computer-I. • Functional English has been shifted from I-Term to II-Term. • Electrical and Electronics Engineering has been shifted from II-Term to I-Term. • Tinkering Lab has been introduced in I-Term. • Advanced programming using C has been replaced by Computer-II. • Basic of Electronics Engineering has been introduced.(All Branches) • Environmental Science and Manufacturing Lab have been shifted from I-Term to II-Term. • Functional English-II has been renamed as English/Humanities-II and shifted to III-Term.
Analog and Digital circuits (same as for Batch 2016-20)	Analog and Digital circuits has been separated and contents have been included into Analog Circuits and Digital Electronics .
Mathematics-III has been removed (same as for Batch 2016-20)	New course Signals and Systems has been introduced in place of Mathematics-III and
Network and Systems (same as for Batch 2016-20)	Network and Systems is replaced by Network Analysis and Synthesis .
New courses have been added (same as for Batch 2016-20)	<ul style="list-style-type: none"> • Introduction to Design (III-Term) • Value and Ethics (III-Term) • Aptitude and Reasoning I – V (From III-Term to VII Term) • English/Humanities-II (III-Term) • Communication Practices-I shifted from III-Term to V- Term • Minor Project-I, II, III. (From IV-Term to VI-Term) & project I has been removed • Open source software (IV-Term)
Project-II in VIII-Term (same as for Batch 2016-20)	Credits of Project II in VIII Term is increased from 8 to 10 Credits.
Open Elective (same as for Batch 2016-20)	Bio Science & Geo Science are included in the list of Open electives for 7 th Term & 8 th Term

AK Singh





SHARDA UNIVERSITY
SCHOOL OF ENGINEERING & TECHNOLOGY
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Date: 04/06/2018

A meeting of board of studies of ECE will be held on 5th June 2018, at 01:00 PM in Room No.005, block I

Chairman: Dr. Pallavi Gupta, HOD

LIST OF PROPOSED MEMBERS OF BOARD OF STUDIES (BOS)

- A. From University**
1. **Prof. Avinashi Kapoor**
Department of Electronic Science
University of Delhi South Campus
New Delhi-110 021
E-mail: avinashi_kapoor@yahoo.com
Mobile: 09350571397
 2. **Dr Ajay Agarwal**
CEERI, CSIR,
Pilani
Mob - 8058598903
- B. From Industry**
1. **Mr. Shekhar Singhal**
Module Lead,
Logic Fruit Technologies,
Sector 30, Gurugram, India
Email ID- shekhar.singhal6@gmail.com
Mobile- 08527801799
 2. **Dr. Rajeev Kumar Srivastava**
Director
Design Platforms, NXP Semiconductors India Private Limited
Information Technology Park
Nagawara Village, Kasaba Hobli
Bangalore 560045
E-mail: rajeev.rajeevsrivastava@gmail.com
Mobile: 07259219237
 3. **Mr. Alok Mittal**
Senior Technical Officer
Advanced manufacturing lab
ST Microelectronics, Greater Noida,
E.Mail: alok.mittal@st.com,
Mobile: 9810060432
- C. Internal BOS members**
1. **Prof. R. M. Mehra**
E-mail: rm.mehra@sharda.ac.in



SHARDA UNIVERSITY
SCHOOL OF ENGINEERING & TECHNOLOGY
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Date: 04/06/2018

A meeting of board of studies of ECE will be held on 5th June 2018, at 01:00 PM in Room No.005, block I

Chairman: Dr. Pallavi Gupta, HOD

LIST OF PROPOSED MEMBERS OF BOARD OF STUDIES (BOS)

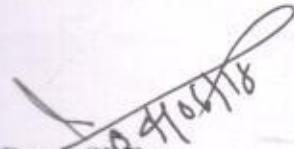
- A. From University**
1. **Prof. Avinashi Kapoor**
Department of Electronic Science
University of Delhi South Campus
New Delhi-110 021
E-mail: avinashi_kapoor@yahoo.com
Mobile: 09350571397
 2. **Dr Ajay Agarwal**
CEERI, CSIR,
Pilani
Mob - 8058598903
- B. From Industry**
1. **Mr. Shekhar Singhal**
Module Lead,
Logic Fruit Technologies,
Sector 30, Gurugram, India
Email ID- shekhar.singhal6@gmail.com
Mobile- 08527801799
 2. **Dr. Rajeev Kumar Srivastava**
Director
Design Platforms, NXP Semiconductors India Private Limited
Information Technology Park
Nagawara Village, Kasaba Hobli
Bangalore 560045
E-mail: rajeev.rajeevsrivastava@gmail.com
Mobile: 07259219237
 3. **Mr. Alok Mittal**
Senior Technical Officer
Advanced manufacturing lab
ST Microelectronics, Greater Noida,
E.Mail: alok.mittal@st.com,
Mobile: 9810060432
- C. Internal BOS members**
1. **Prof. R. M. Mehra**
E-mail: rm.mehra@sharda.ac.in

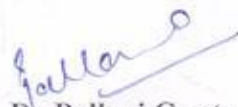
- Mobile: 9971271374
2. **Dr. Rashmi Priyadarshini**
E-mail: rashmi.priyadarshini@sharda.ac.in
Mobile: 9911450973
 3. **Mr. Sandeep Singh**
E-mail: sandeepsingh.ec@sharda.ac.in
Mobile: 9891833637
 4. **Mrs. Shaheen Naaz**
Email: Shaheen.naaz@sharda.ac.in
Mobile: 989899454758
 5. **Mr. Shiv Pujan Jaiswal**
EMail: shivapujan.jaiswal@sharda.ac.in
Mobile: 8826998122
 6. **Mr. C. Mohan**
EMail: c.mohan@sharda.ac.in
Mobile: 9811090352

Following are the agenda of the meeting :

1. Approval of new Course Structure of B.Tech., Electronics & Communication Engineering for 2018 Batch. (160 Credit)
2. Approval of new program of B.Sc (Hons) Electronics with specialization in Robotics and Application.
3. Approval of Syllabi of Above programs.
4. Approval of few syllabi of B.Tech. Electrical & Electronics Engineering, B.Tech. CSE with specialization in IoT.
5. Approval of few syllabi of M. Tech. Programmes.
6. Any other item.

All members are requested to make it convenient to attend the meeting.


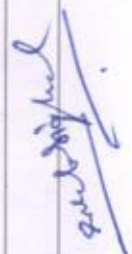
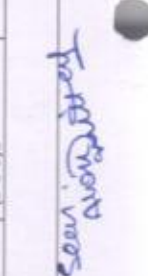

Dean, SET


Dr. Pallavi Gupta

HOD, ECE & EE Deptt.

B.Tech Course Structure for session ECE (2018 onwards)

Sem.	Courses (L-T-P)											Courses	Contact Hours			Credits							
	Introduction to Engineering SC1 (0-0-2)1	Programming for Problem Solving SC2 (2-0-4)4 (3-0-2)	Environmental Science SC7 (2-0-0)2	Maths (Bucket based) SC4 (3-1-0)4	Engineering Physics SC8 (2-1-2)4	Computer Aided Design & Drafting SC12 (0-0-3)1.5	Principles of Electrical and Electronics Engineering SC11 (2-1-2)4	Soft Skill - 1 SC16 (0-0-4)2	Maths (Bucket based) SC5 (3-1-0)4	Engineering Chemistry SC10 (3-0-2)4	Mechanical Workshop SC13 (0-0-3)1.5		Universal Human Values and Ethics SC15 (2-0-0)2	Soft Skill - 2 SC17 (0-0-4)2	Design/creativity course based on requirement SC24 (0-0-2)1		summer Internship (0-0-2)1	8	L	T	P	Total	22.5
II	Application based Programming in Python SC3 (3-0-2)4	Maths (Bucket based) SC5 (3-1-0)4	Advanced Physics (Electromagnetics) SC9 (2-1-2)4	Engineering Chemistry SC10 (3-0-2)4	Mechanical Workshop SC13 (0-0-3)1.5	Universal Human Values and Ethics SC15 (2-0-0)2	Soft Skill - 2 SC17 (0-0-4)2	Design/creativity course based on requirement SC24 (0-0-2)1	summer Internship (0-0-2)1	9	12	2	19	33	23.5								
III	Soft Skill - 3 SC18 (0-0-4)2	Introduction to Biology for Engineers SC14 (2-0-0)2	Maths III (probability) SC6 (3-0-2)4	Analog Electronics PC1 (3-0-2)4	Network Theory PCS (3-0-2)4	Electrical and Electronics Measurement PC3 (3-0-2)4	Project Based Learning (PBL) -1 PC13 (0-0-1)1	Project Based Learning (PBL) -2 PC14 (0-0-1)1	Project Based Learning (PBL) -3 PC15 (0-0-2)1	Project Based Learning (PBL) -4 PC16 (0-0-2)1	Open Elective - 1 OE1 (2-0-0)2	Open Elective - 2 OE2 (2-0-0)2	summer Internship (0-0-2)1	7	14	0	13	27	21				
IV	Soft Skill - 4 SC19 (0-0-4)2	Signal and Systems PC4 (3-1-0)4	Digital System Design PC2 (3-0-2)4	Communication Engineering PC6 (3-0-2)4	Microprocessor and Microcontroller with Interfacing PC7 (3-0-2)4	Management Course (from basket) SC23 (3-0-0)3	Project Based Learning (PBL) -2 PC14 (0-0-1)1	Project Based Learning (PBL) -3 PC15 (0-0-2)1	Project Based Learning (PBL) -4 PC16 (0-0-2)1	Open Elective - 1 OE1 (2-0-0)2	Open Elective - 2 OE2 (2-0-0)2	summer Internship (0-0-2)1	8	15	1	13	29	23					
V	Soft Skill - 5 SC20 (0-0-4)2	Control system PC8 (3-0-2)4	Digital Signal Processing PC9 (3-0-2)4	Computer Architecture PC10 (3-0-2)4	Program Elective-1 PE1 (3-0-0)3	Technical Skill Enhancement Course-1 PC17 (0-0-2)1	Project Based Learning (PBL) -3 PC15 (0-0-2)1	Project Based Learning (PBL) -4 PC16 (0-0-2)1	Open Elective - 1 OE1 (2-0-0)2	Open Elective - 2 OE2 (2-0-0)2	summer Internship (0-0-2)1	8	14	5	14	33	21						
VI	Soft Skill - 6 SC21 (0-0-4)2	Digital Communication PC11 (3-0-2)4	Computer networks PC12 (4-0-0)4	Technical Skill Enhancement Course-2 PC18 (0-0-2)1	Program Elective-2 PE2 (3-0-0)3	Program Elective-3 PE3 (3-0-0)3	Project Based Learning (PBL) -3 PC15 (0-0-2)1	Project Based Learning (PBL) -4 PC16 (0-0-2)1	Open Elective - 1 OE1 (2-0-0)2	Open Elective - 2 OE2 (2-0-0)2	summer Internship (0-0-2)1	8	14	0	12	26	20						
VII	Program Elective-4 PE4 (3-0-0)3	Program Elective-5 PE5 (3-0-0)3	Major Project-1 SC26 (0-0-6)3	Comprehensive Examination SC22 (0-0-0)0	Professional Ethics and Values SC28 (0-0-0)0	Industrial Internship SC25 (0-0-2)1	Open Elective -3 OE3 (3-0-0)3	Soft Skill-7 SC22 (0-0-2)1	summer Internship (0-0-2)1	8	10	0	8	18	14								
VIII	Major Project - 2 SC27 (0-0-18)3	Program Elective-6 PE6 (3-0-0)3	Open Elective - 4 OE-4 (3-0-0)3	Comprehensive Examination SC22 (0-0-0)0	Professional Ethics and Values SC28 (0-0-0)0	Industrial Internship SC25 (0-0-2)1	Open Elective -3 OE3 (3-0-0)3	Soft Skill-7 SC22 (0-0-2)1	summer Internship (0-0-2)1	8	6	0	18	29	15								
															TOTAL			160					


 seen approved

 sent to school

 TOTAL

Minutes of Meeting of BOS: B.Tech Electrical and Electronics Engineering

1- Java Course for electrical Engineering for Batch admitted in 2016-17 is changed to 'Object oriented programming using Java' and open source course is changed by 'Introduction to Sensors and Transducers'

2-Java Course for Batch admitted in 2017-18 is changed to 'Industrial Instrumentation' and open source course is changed by 'Introduction to Sensors and Transducers'

2016/17
5/6/18

5/6/18

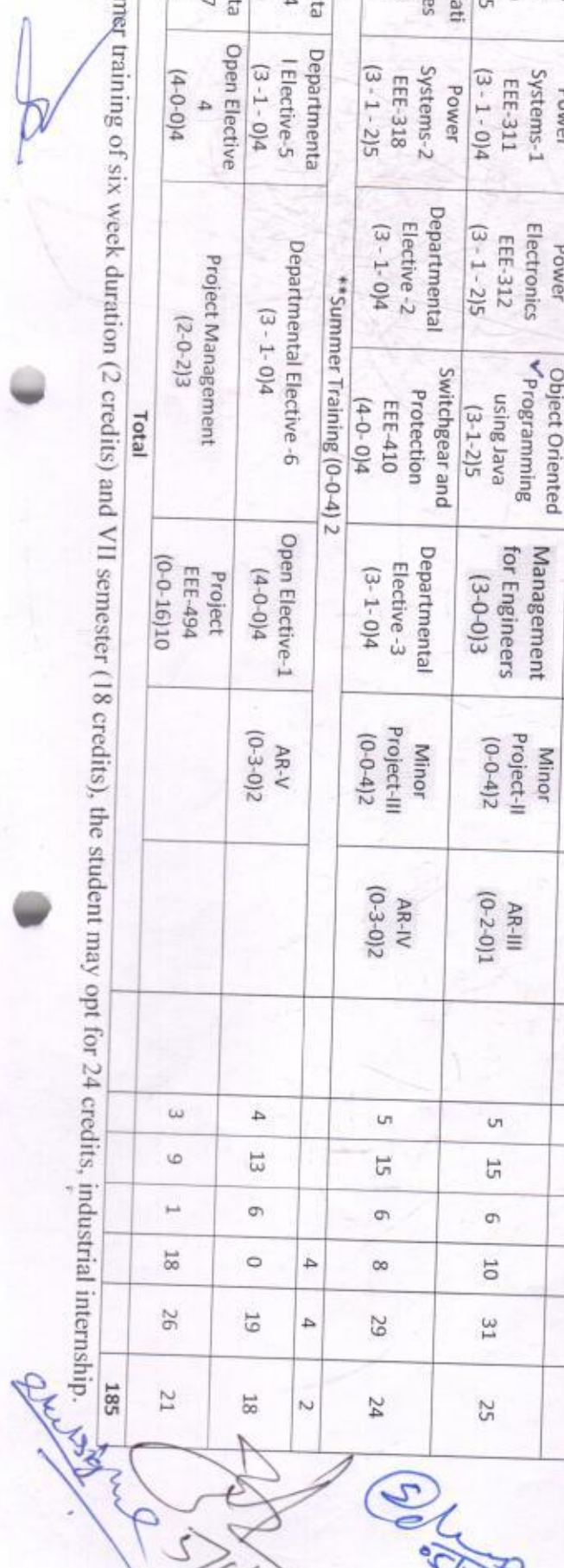
65/06/18

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**Proposed COURSE AND CREDIT STRUCTURE-B.Tech(EEE)
for batch admitted in 2016 (Students admitted in 2016-17)**

Sem.											Courses	L	T	P	Weekly Contact	Credits			
I	Physics-II PHY-112 (3-1-2)5	Logic Building & problem Solving CSE-105 (3-1-2)5	Mathematics-I MTH-112 (3-2-0)4	Env. Sciences EVS-103 (2-0-0)2	Manufacturing Science EEE-154 (0-0-4)2	Functional English-I ENG-102 (2-0-2)3						5	13	4	10	27	21		
II	Chemistry CHY-105 (3-1-2)5	Programming using C CSE-106 (3-0-2)4	Mathematics-II MTH-113 (3-2-0)4	Physics PHY-114 (3-1-0)4	PEEE EEE-110 (3-1-2)5	Functional English-II ENG-103 (2-0-2)3						6	17	5	8	30	25		
III	Electrical Machines-1 EEE-210 (3-1-2)5	EMFT EEE-211 (3-1-0)4	Electrical and Electronic Measurement EEE-215 (3-1-2)5	Analog & Digital Electronics EEE-212 (3-1-2)5	Humanities (2-0-2)3	Values and Ethics (2-0-0)2	AR-I (0-2-0)1					6	16	6	8	30	25		
IV	Electrical Machines-2 EEE-213 (3-1-2)5	Network Analysis & Synthesis EEE-214 (3-1-0)4	Departmental Elective-1 (3-1-0)4	Microprocessor EEE-319 (3-1-2)5	Introduction to Sensors & Transducers (2-0-0)2	Open Source Software (0-0-2)1	Minor Project-I (0-0-4)2	AR-II (0-2-0)1				5	14	6	10	30	24		
V	Control System EEE-310 (3-1-2)5	Power Systems-1 EEE-311 (3-1-0)4	Power Electronics EEE-312 (3-1-2)5	Object Oriented Programming using Java (3-1-2)5	Management for Engineers (3-0-0)3	Minor Project-II (0-0-4)2	AR-III (0-2-0)1					5	15	6	10	31	25		
VI	Communications on Practices ENG-202 (2-0-2)3	Power Systems-2 EEE-318 (3-1-2)5	Departmental Elective-2 (3-1-0)4	Switchgear and Protection EEE-410 (4-0-0)4	Departmental Elective-3 (3-1-0)4	Minor Project-III (0-0-4)2	AR-IV (0-3-0)2					5	15	6	8	29	24		
**Summer Training (0-0-4) 2																			
VII*	Departmental Elective-4 (3-1-0)4	Departmental Elective-5 (3-1-0)4	Departmental Elective-6 (3-1-0)4	Open Elective-1 (4-0-0)4	AR-V (0-3-0)2							4	4	0	19	2	18		
VIII	Departmental Elective-7 (3-1-0)4	Open Elective 4 (4-0-0)4	Project Management (2-0-2)3	Project EEE-494 (0-0-16)10								3	9	1	18	26	21		
Total																			185

**In lieu of summer training of six week duration (2 credits) and VII semester (18 credits), the student may opt for 24 credits, industrial internship.



Proposed COURSE AND CREDIT STRUCTURE-B. Tech(EEE) for batch admitted in 2017
STUDENTS ADMITTED IN 2017-2018

Sem.	Proposed COURSE AND CREDIT STRUCTURE-B. Tech(EEE) for batch admitted in 2017										Courses	L	T	P	Weekly Contact	Credits	
I	Maths-I (3-1-0)4	Computer-I (2-1-4)4	Physics-I (3-1-0)4	Principle of Electrical Engg. (3-1-2)5	Chemistry (3-0-2)4	Tinkering Lab (E) (0-0-2)1						5	14	4	12	30	22
II	English/ Humanities-I (2-0-2)3	Maths-II (3-1-0)4	Computer-II (2-0-4)4	Physics-II (3-0-2)4	Basics of Electronics and Communication Engg. (3-0-0)3	Energy Cons. And Environment Sc. (3-0-0)3	Manufacturing Practices(E) (0-0-4)2					6	16	1	12	29	23
III	Electrical Machines-1 EEE-210 (3-1-2)5	EMFT EEE-211 (3-1-0)4	Electrical and Electronic Measurement EEE-215 (3-1-2)5	Analog & Digital Electronics EEE-212 (3-1-2)5	English/ Humanities-II (2-0-2)3	Values and Ethics (2-0-0)2	AR-I (0-2-0)1					6	16	6	8	30	25
IV	Electrical Machines-2 EEE-213 (3-1-2)5	Network Analysis & Synthesis EEE-214 (3-1-0)4	Departmental Elective-1 (3-1-0)4	Microprocessor EEE-319 (3-1-2)5	Introduction to Sensors & Transducers (2-0-0)2	Open Source Software (0-0-2)1	Minor Project-I (0-0-4)2	AR-II (0-2-0)1				5	14	6	10	30	24
V	Control System EEE-310 (3-1-2)5	Power Systems-1 EEE-311 (3-1-2)5	Power Electronics EEE-312 (3-1-2)5	Industrial Instrumentation (3-1-0)4	Management for Engineers (3-0-0)3	Minor Project-II (0-0-4)2	AR-III (0-2-0)1					5	15	6	10	31	25
VI	Communication Practices ENG-202 (2-0-2)3	Power Systems-2 EEE-318 (3-1-2)5	Departmental Elective-2 (3-1-0)4	Switchgear and Protection EEE-410 (4-0-0)4	Departmental Elective-3 (3-1-0)4	Minor Project-III (0-0-4)2	AR-IV (0-3-0)2					5	14	6	10	30	24
**Summer Training (0-0-4) 2																	
VII*	Departmental Elective-4 (3-1-0)4	Departmental Elective-5 (3-1-0)4	Departmental Elective-6 (3-1-0)4	Open Elective-1 (4-0-0)4	AR-V (0-3-0)2							4	13	6	0	19	18
VIII	Departmental Elective-7 (3-1-0)4	Open Elective 4 (4-0-0)4	Project Management (2-0-2)3	Project EEE-494 (0-0-16)10								3	9	1	18	26	21
Total																	184

**In lieu of summer training of six week duration (2 credits) and VII semester (18 credits), the student may opt for 24 credits, industrial internship, *gaining*

gaining

gaining
 10/02/20
 10/02/20

Department of Electronic and Communication Engineering
School of Engineering and Technology
Sharda University
Minutes of Meeting of External BoS(session 2018 onwards)

Following members were present in the External BoS for various programs on 05/06/2018 at 005, board room, block-I Chaired by Dr. Pallavi Gupta, HoD, ECE&EEE

A. From other University

1. Prof. Avinashi Kapoor
Department of Electronic Science
University of Delhi South Campus
New Delhi-110 021
E-mail: avinashi_kapoor@yahoo.com
Mobile: 09350571397

2: Dr Ajay Agarwal

CEERI, CSIR,
Pilani
Mob o8058598903

B. From Industry

1. **Mr. Shekhar Singhal**
Module Lead,
Logic Fruit Technologies,
Sector 30, Gurugram, India
Email ID- shekhar.singhal6@gmail.com
Mobile- 08527801799

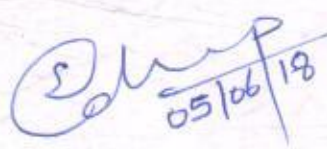
2. Dr. Rajeev Kumar Srivastava
Director
Design Platforms, NXP Semiconductors India Private Limited
Information Technology Park
Nagawara Village, Kasaba Hobli
Bangalore 560045
E-mail: rajeev.rajeevsrivastava@gmail.com
Mobile: 07259219237

3. Mr.Alok Mittal
Senior Technical Officer
Advanced manufacturing lab

ST Microelectronics
Greater Noida
E.Mail:Alok.mittal@st.com
Mobile:9810060432

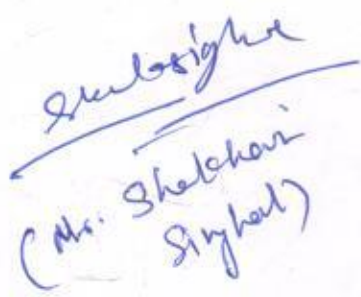
C. Internal members

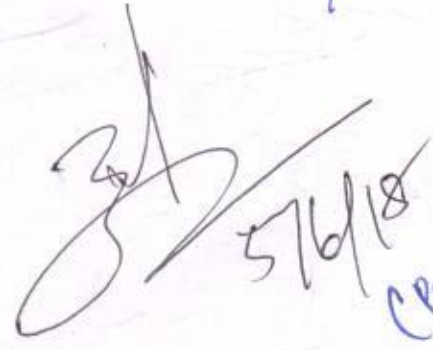
1. Dr. Pallavi Gupta(HOD)
(Associate Professor)
2. Prof. R. M. Mehra
3. Dr. Rashmi Priyadarshini
4. Mr. Sandeep Singh
5. Mrs. Shaheen Naaz
6. Mr. Shiv Pujan Jaiswal
7. Mr. C Mohan
8. Ms Manisha Rajoriya
9. Ms. Ritu Singh
10. Mr. Prince Nagar


05/06/18

Manisha Rajoriya




(Mr. Shalochan Singh)


5/6/18
(Prof. Anilashi Kapur)







Minutes B.Tech Course Structure - ECE

1. In Introduction to Engineers syllabus to be modified including Blue Brain, 5G, in place of AI.
2. Include subject as Smart Materials as elective.
3. Summer Internship in 1st year is to be reviewed.
4. Soft skill may be changed to audit course, to save our credits for core subjects like LIC.
5. Introduction to biology may be changed to Biomedical Engineering.
6. In DSP Book, include book DSP BY Rabiner and Gold, Pearson publication.
7. In tinkering lab syllabus, update syllabus of unit 3 and 4.
8. In Lab we should include protocol analyzer (open source).
9. PCB Design tools may be incorporated in syllabus of Tinkering lab.
10. Security and power saving techniques may be included in syllabus of IOT and Basics of drone technology
11. In 6th semester, if students are taking Embedded system as an elective, the project based learning will be lab for Embedded system based on STM32.
12. There should be one additional subject may be included in the program including differential amplifier, level shifter and current mirror and CMOS Technology.

Sen
not
initial

Mulligal

Edup
05/06/18

Pallavi

for

[Signature]